

# Semiconductor Nanowires for Energy Conversion

Allon I. Hochbaum<sup>\*,†</sup> and Peidong Yang<sup>\*</sup>

Department of Chemistry, University of California, Berkeley, California 94720 and Materials Sciences Division, Lawrence Berkeley National Laboratory, Berkeley, California 94720

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## 1. Introduction: Role of Materials in Energy Conversion

Between 2004 and 2030 the annual global consumption of energy is estimated to rise by more than 50%.<sup>1</sup> Assuming current policies and practices remain in place, most of the increased energy production is expected to come from the combustion of fuels, such as oil, ethanol, natural gas, and coal. A commensurate increase in CO<sub>2</sub> (a prominent greenhouse gas) emissions is anticipated, much of which is due to burning coal—the fastest growing source of energy globally. Despite projected persistent increases in oil and gas prices, less than 10% of the global energy production in 2030 is predicted to come from renewable energy sources, such as hydroelectric, solar, wind, hydrothermal, and biomass. In order to moderate global reliance on exhaustible natural resources and their environmentally hazardous combustion, more scientific efforts should be directed toward reducing the cost of energy production from renewable sources.

There exist many potential renewable energy technologies in the form of solid-state devices, such as, for example, solar cells, which convert solar energy in the form of light to the more practical form of electricity. In addition, a large collection of condensed matter phenomena involve the conversion of energy from one form to another, and some proceed with efficiency near unity. Consequently, the study of energy conversion in materials is a field full of opportunities for practical and socially significant applications. Within the last couple of decades there has been increasing interest

in materials with nanometer-scale dimensions. Semiconductor nanowires, a subset of these materials, have received exceptional attention for their unique properties and complex structures. Many nanowire-based materials are promising candidates for energy conversion devices.

Since electricity is ubiquitously used to power machines and instruments which do work for humans, the practical end product of many energy conversion processes is electrical energy. Even in devices which produce other forms of energy, such as chemical fuel, the conversion process often requires, at least as an intermediate energy state, an electrical potential. For nanowire materials, a fundamental understanding of transport in quasi-one-dimensional structures will be crucial to the development of new energy conversion technologies. Moreover, as global power consumption increases, there will be an increasing need for new energy storage schemes. Pushing electrons across power grids may not be the ideal solution for many alternative energy technologies. Rather, electrical energy storage in batteries and chemical fuels seems to be a fruitful research direction. To this end, a fundamental understanding of the electrochemical properties and electron transfer characteristics of nanostructured electrodes and catalysts will be essential. The following sections will review ways in which semiconductor nanowires can enable advanced solid-state energy conversion and storage devices. The discussion of nanowire-based devices will focus on photovoltaic, thermoelectric, and chemical energy storage materials, namely, the conversion of light energy, heat energy, and the energy stored in chemical bonds to electrical potentials and vice versa.

## 2. Why Are Semiconductor Nanowires Special?

The term ‘nanowire’ is generally used to describe a large aspect ratio rod 1–100 nm in diameter (Figure 1). Both descriptors are pertinent to the physical and technological significance of nanowires. First, the diameter puts the radial dimension of these structures at or below the characteristic length scale of various interesting and fundamental solid-state phenomena: the exciton Bohr radius, wavelength of light, phonon mean free path, critical size of magnetic domains, exciton diffusion length, and others.<sup>2,3</sup> As a result, many physical properties of semiconductors are significantly altered within the confines of the nanowire surfaces. In addition, their large surface-to-volume ratio allows for distinct structural and chemical behavior as well as greater chemical reactivity. This two-dimensional confinement endows nanowires with unique properties which stray from those of their corresponding bulk material. Second, the large aspect ratio of nanowires intimates their technological application. The one unconstrained dimension can direct the

\* To whom correspondence should be addressed. E-mail: hochbaum@seas.harvard.edu (A.H.), p\_yang@berkeley.edu (P.Y.).

† Current address: School of Engineering and Applied Sciences and Department of Chemistry and Chemical Biology, Harvard University, Cambridge, MA 02138.

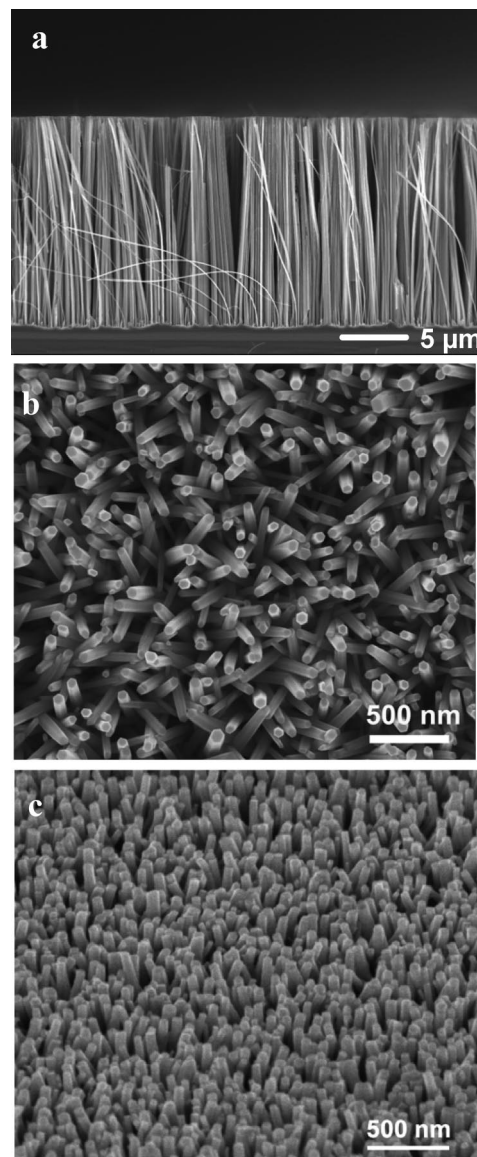


Allon Hochbaum received his S.B. degree in Materials Science and Engineering from the Massachusetts Institute of Technology in 2003. He joined Professor Peidong Yang's lab and received his Ph.D. degree from the Department of Chemistry at U.C. Berkeley in 2008, partly funded by an NSF IGERT fellowship in Nanoscience and Nanotechnology. He is currently a postdoctoral research fellow at Harvard University, investigating biological and materials interfaces. His research interests include the design and synthesis of nanometer-scale materials with unique properties for energy conversion and biological applications.



Peidong Yang received his B.S. degree in Chemistry from the University of Science and Technology of China in 1993 and Ph.D. degree in Chemistry from Harvard University in 1997. He did postdoctoral research at the University of California, Santa Barbara, before joining the faculty in the Department of Chemistry at the University of California, Berkeley, in 1999. He is currently Professor in the Department of Chemistry, Materials Science and Engineering, and a senior faculty scientist at the Lawrence Berkeley National Laboratory. He is the deputy director for the Center of Integrated Nanomechanical Systems. He is an associate editor for the *Journal of the American Chemical Society* and also serves on the Editorial Advisory Board for a number of journals including *Accounts in Chemical Research* and *Nano Letters*. He was the first chairperson for the newly established Nanoscience subdivision within the American Chemical Society. He is the recipient of the Alfred P. Sloan research fellowship, Arnold and Mabel Beckman Young Investigator Award, National Science Foundation Young Investigator Award, MRS Young Investigator Award, Julius Springer Prize for Applied Physics, ACS Pure Chemistry Award, and Alan T. Waterman Award. His main research interest is in the area of one-dimensional semiconductor nanostructures and their applications in nanophotonics, nanoelectronics, energy conversion, and nanofluidics.

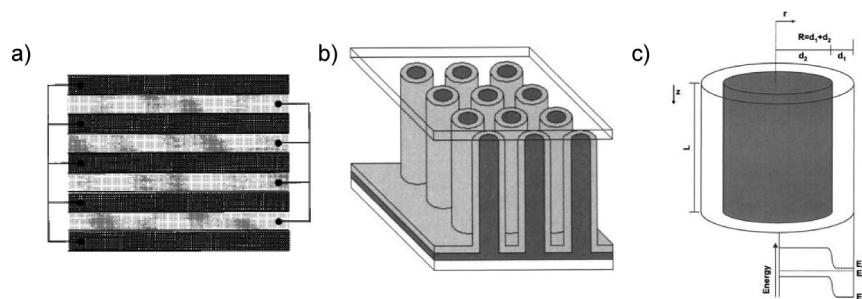
conduction of quantum particles such as electrons, phonons, and photons. This control over various forms of energy transport recommends nanowires as ideal materials from which to manufacture advanced solid-state devices. Moreover, nanowire lengths are normally sufficient to interface with top-down fabrication processes, such as photolithography. As a result, nanowires provide a convenient platform through which researchers may study confined transport phenomena.



**Figure 1.** Scanning electron microscopy images of Si, ZnO, and InGaN nanowires, important semiconductor materials for energy conversion and storage.

### 3. Electrical and Thermal Transport in Nanowires

For the past decade, exquisite synthetic control has enabled researchers to specifically tailor nanowire structures and observe a wide range of electron transport phenomena. By fine tuning high-quality heterostructures and interfaces within nanowires, researchers have been able to track single-electron transport. With small nanowire diameters, the volume occupied by conduction electrons is significantly condensed. When the longitudinal dimension is also constrained, in this case by the electronic band offset between axial heterojunctions, these electrons interact more strongly and repel each other. In such cases, discrete amounts of energy are required to push electrons into these confined volumes and individual electrons can be manipulated by applied voltages.<sup>4</sup> Electron transport phenomena such as Coulomb blockades<sup>5,6</sup> and resonant tunneling<sup>7</sup> have been observed in such systems. These properties are useful for fabricating advanced solid-state devices such as resonant tunneling diodes<sup>7</sup> and single-electron transistors.<sup>8</sup> Additionally, coaxial heterostructures can be fabricated controllably to produce a one-dimensional hole gas, which is useful for high-performance field-effect



**Figure 2.** Heterojunction morphologies for improved charge collection. (a) Multilayer heterojunctions require multiple deposition steps and postgrowth processing to contact individual layers. (b) Vertically aligned radial heterostructures require minimal processing steps and successfully decouple the absorption and minority charge carrier diffusion lengths. (c) The diameter of the p- and n-type core and shell can be optimized depending on the trap density in the layers. (Reprinted with permission from ref 15. Copyright 1995 Wiley. Reprinted with permission from ref 17. Copyright 2005 AIP.)

transistors.<sup>9</sup> Nanowires have also been shown to support correlated electron transport, such as superconductivity.<sup>10</sup> Synthetic techniques have been developed to the point where electronic confinement and unique properties are readily achieved, and ongoing research is in finding new ways to use and integrate these nanowires into functional devices.

Interesting size-dependent behavior has also been observed for thermal transport in nanowires. Li and co-workers<sup>11,12</sup> discovered that the thermal conductivity of vapor–liquid–solid (VLS) grown Si nanowires is strongly diameter dependent. They found that the thermal conductivity of small diameter nanowires is up to 10-fold lower than the bulk value. Furthermore, the temperature dependence of the thermal conductivity at low temperature is highly anomalous for thin wires. Whereas the thermal conductivity is expected to scale as  $\sim T^3$ , following the low-temperature heat capacity, it scales as  $\sim T^2$  or even linearly with temperature for the smallest diameter nanowires. Though this phonon transport is not as well understood as nanoscale electronic phenomena, these results suggest that heat conduction in confined systems is a relatively unexplored and intriguing field of study. It is these unique charge and thermal transport phenomena that make semiconductor nanowires promising as building blocks for many of the energy conversion and storage devices reviewed in this manuscript. Not all energy conversion schemes are put to use, many as a result of prohibitive costs. Consequently, the goal of much of the following work is twofold: (1) to study fundamental energy conversion processes in high-quality, well-characterized nanowire systems and (2) to exploit the size dependence of nanowire transport properties to fashion efficient devices using inexpensive and scalable materials and syntheses.

## 4. Nanowire Photovoltaic Devices

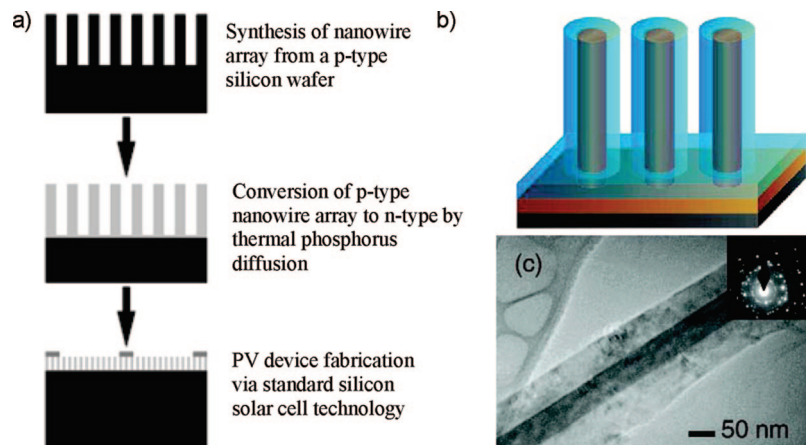
### 4.1. Conventional Solar Cells

Although most commercial solar cells are fabricated from silicon, it has a relatively low absorption coefficient throughout much of the visible and near-infrared parts of the electromagnetic spectrum.<sup>13,14</sup> Because the vast majority of the light from the sun is in this range, silicon-based solar cells must be thick in order to collect most of the incident photons. For example, a silicon cell must be several micrometers to millimeters thick to absorb 90% of the incident light at wavelengths from 700 to 1100 nm, respectively, which comprise about one-half of the solar energy available above the band gap of silicon.<sup>13,15</sup> Conventional solar cells are constructed from planar junctions of p- and

n-type semiconductors, where the electrostatic potential at the interface provides the driving force for charge separation. The collection efficiency of charge carriers generated at a given distance from the junction, though, depends on the minority carrier diffusion length in the n- and p-type quasi-neutral regions. The minority carrier diffusion length is limited by various recombination mechanisms within the silicon lattice and their associated relaxation times. To minimize carrier recombination and thus optimize collection efficiency, cells must be constructed of high-purity single-crystalline silicon with a large minority carrier diffusion length. Though highly efficient cells have been fabricated in the laboratory, practical application of such devices is limited due to the cost of producing and refining such pure material and the associated device fabrication.

A proposed solution to this problem relies on decoupling the long extinction distance of silicon and the proximity of generated charge carriers to the p–n junction. Devices that absorb photons and collect charges along orthogonal directions meet this condition. Parallel multijunction cells, for example, address this issue by connecting thin, alternating n- and p-type vertically stacked layers in parallel with charges extracted laterally.<sup>16</sup> Though such devices have theoretical charge collection efficiencies near unity and are highly defect tolerant, the fabrication process requires multiple thin-film deposition steps that may preclude their widespread use. Such a scheme can also be realized by fabricating p–n junctions that lie normal to the substrate surface, such as an array of silicon posts composed of core and shell regions of opposite carrier polarity,<sup>17</sup> as shown in Figure 2. Charges generated in this structure could be extracted from both quasi-neutral regions by contacting separately the substrate, connected to the core, and the top surface of the posts, connected to the shell. Although synthetic methods such as VLS-CVD growth could be used to fabricate such arrays, the required dimensions for these structures are not necessarily nanometer scale.<sup>18</sup> The optimal post diameter is about twice the minority carrier diffusion length, which is hundreds of nanometers to several micrometers even at high carrier densities ( $\sim 10^{18}$  cm<sup>-3</sup>).<sup>17,19</sup> Even bottom-up-synthesized silicon nanowires, which may contain high concentrations of metal impurity trap sites, exhibit minority carrier diffusion lengths up to several micrometers that appear to be surface-recombination limited.<sup>19,20</sup> At this size scale, these post arrays should be attainable with conventional top-down photolithographic methods.

Truly nanoscale wires offer performance enhancements when the minority carrier diffusion length drops significantly



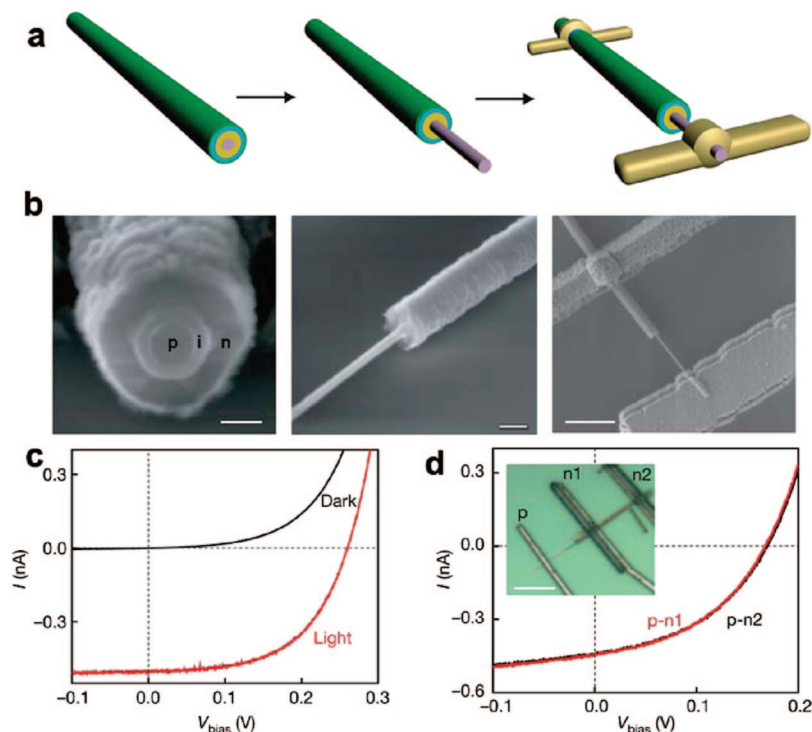
**Figure 3.** Nanowire array solar cells. (a) Schematic of a subsurface p–n junction device fabrication process. (b) Schematic of a radial heterostructure nanowire array, and TEM image of one of the synthesized nanowires showing the n-type crystalline core and p-type polycrystalline shell. (Reprinted with permission from ref 29. Copyright 2005 Wiley. Reprinted with permission from ref 38. Copyright 2008 American Chemical Society.)

as is the case with highly defective silicon. For many applications the critical parameter of power generation is cost per Watt. Even relatively inefficient solar cells, therefore, are practical as long as their fabrication costs are low enough. The cost of silicon and substrate processing comprises 10–50% of the total cost of a solar cell,<sup>14,21,22</sup> and much of the cost is due to the purification process, as metallurgical grade silicon (99–99.99% purity, or about  $10^{19}$ – $10^{21}$  impurities per  $\text{cm}^3$ ) is 50 times less expensive than higher grade silicon.<sup>23</sup> Consequently, the price per Watt of power from conventional solar cells could drop significantly if these devices tolerated higher defect concentrations.<sup>24</sup> The power conversion efficiency of a solar cell is given as  $\eta = (FFJ_{\text{SC}}V_{\text{OC}})/P_{\text{in}}$ , where  $FF$  is the fill factor,  $J_{\text{SC}}$  is the current density at short circuit ( $V = 0$ ),  $V_{\text{OC}}$  is the photovoltage at open circuit ( $I = 0$ ), and  $P_{\text{in}}$  is the incident light power density. The main cause of efficiency losses from lattice defects in silicon solar cells is an increase in the recombination current resulting from minority carrier recombination at trap sites. Recombination currents can account for a 25% or more decrease in overall cell efficiency, depending on the energy of the impurity acceptor or donor states, due to lower  $V_{\text{OC}}$  and  $FF$ .<sup>25,26</sup> This recombination current varies as  $I_{\text{rec}} \propto (L_n L_p)^{-1}$ , where  $L_{n/p}$  is the minority carrier diffusion length for n- or p-type carriers and  $L_{n/p} \propto N_t^{-1/2}$ , where  $N_t$  is the carrier trap density.<sup>26,27</sup> Photovoltaic devices made from nanostructured dirty silicon would benefit from shorter minority carrier conduction lengths that might mitigate this problem.

Several studies of silicon nanowire-based solar cells primarily explore the advantages of greater light absorption within the nanowire array. The vertical array geometry scatters light efficiently, especially at short wavelengths, and, depending on the nanowire dimensions, can absorb more light than a comparably thick solid crystalline film.<sup>28</sup> This result has been born out in several studies where the p–n junction lies below the surface of the wafer upon which the array stands.<sup>29–31</sup> Since nanowires do not span the junction in these devices, the cells behave like conventional single-crystalline p–n junction cells, only with greater absorption of incident light. These cells, schematically shown in Figure 3a, do not reach the efficiencies of conventional cells for a couple reasons. First, the junctions made in the lab by diffusion doping are generally of poor quality, so the  $V_{\text{OC}}$  suffers significantly.<sup>29,31</sup> Second, charge extraction occurs

through the nanowires, decreasing  $J_{\text{SC}}$  due to the larger series resistance of the nanowires and their contacts. The latter effect is especially pronounced in the VLS-grown nanowires<sup>30,32</sup> because they cover very little of the substrate surface, thus reducing the conduction cross-section. Consequently, their efficiencies are 0.1% or less. Mg-doped GaN nanowires grown on silicon substrates have analogous antireflection properties and power conversion efficiencies of nearly 3%.<sup>33</sup> These cells also possess larger internal fields, improving  $V_{\text{OC}}$  to 0.95 V. This work is also promising for fabricating photovoltaic devices with lattice-mismatched materials since nanowires can withstand more strain than corresponding thin films.<sup>34–36</sup>

Silicon nanowire radial heterostructures, on the other hand, are the optimal design for efficient charge collection. These structures have been realized with VLS-grown<sup>32,37</sup> and electrolessly etched silicon nanowire arrays.<sup>38</sup> Both methods first synthesize the nanowires and then deposit conformal polycrystalline thin films of intrinsic or complementary polarity silicon. Garnett and co-workers fabricated radial p–n junctions, shown in Figure 3b and c, on nanowire arrays synthesized by a scalable, aqueous solution etching synthesis<sup>39</sup> which exhibit cell efficiencies of about 0.5%. The low efficiency is due partly to interfacial recombination losses (decreasing  $V_{\text{OC}}$  and  $FF$ ), as evidenced by a significant dark current, and partly to a large series resistance in the polycrystalline shell (decreasing  $J_{\text{SC}}$ ).<sup>38</sup> Tian and co-workers synthesized similar nanowires with p–n and p–i–n radial heterostructures by the VLS mechanism and subsequent thin film deposition and characterized the photovoltaic response of individual nanowires (Figure 4). Though  $V_{\text{OC}}$  and  $J_{\text{SC}}$  were not reported for the p–n device, it is clear from the ideality factor and breakdown voltage that the p–i–n heterostructure contains a more robust junction. The  $V_{\text{OC}}$  of this device is similar to the radial p–n nanowire arrays of Garnett et al., but  $J_{\text{SC}}$  is four to five times greater, possibly a result of significantly lower polycrystalline shell resistivity. The device efficiency was calculated to be between 2.3% and 3.4%. Additionally, Tian and co-workers demonstrated a single-nanowire photovoltaic device by selective chemical etching and contacting the p- and n-type regions separately. Moreover, this device was able to power a logic circuit and a nanowire pH sensor on a self-contained circuit.<sup>37</sup>



**Figure 4.** (a) Schematics of single nanowire PV device fabrication. (Left) Pink, yellow, cyan, and green layers correspond to the p-core, i-shell, n-shell, and PECVD-coated SiO<sub>2</sub>, respectively. (Middle) Selective etching to expose the p-core. (Right) Metal contacts deposited on the p-core and n-shell. (b) SEM images corresponding to schematics in a. Scale bars are 100 nm (left), 200 nm (middle), and 1.5 μm (right). (c) Dark and light  $I$ - $V$  curves for two different n-shell contact locations. (Inset) Optical microscopy image of the device. Scale bar, 5 μm. (Reprinted with permission from ref 37. Copyright 2007 Nature.)

## 4.2. Excitonic Solar Cells

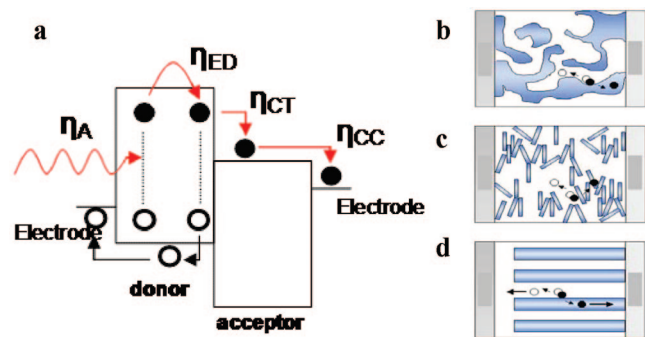
Another type of photovoltaic device, known as an excitonic cell,<sup>40,41</sup> in which small molecules, polymers, or quantum dots are used as light absorbing materials, could also benefit from incorporation of nanowire components. Electronic excitations, by incident light, in these cells produce bound electron-hole pairs called excitons. For comparison, excitons in silicon have a binding energy of roughly 20 meV, whereas thermal energy at room temperature ( $kT$ ) is approximately 25 meV. Consequently, illumination of silicon generates free carriers in the bulk, which segregate to the electrodes due to the built-in potential of the p-n junction. In contrast, the weak intermolecular interactions and low dielectric constants of organic dyes, small molecules, and polymers lead to greater localization of photoexcited states.<sup>42</sup> With exciton binding energies greatly exceeding  $kT$ , illumination of excitonic solar cells generates tightly bound electron-hole pairs. In order to separate charge, the exciton must diffuse to the junction without recombining. Exciton dissociation occurs as long as the band offset energy at the junction is greater than the exciton binding energy in the material from which it was generated. The exciton splitting in these devices is further limited by the kinetics of charge injection through the junction. As a result, the  $V_{OC}$ , and partly  $J_{SC}$ , depend on the relative rates of electron and hole transfer across the interface.<sup>41</sup>

Using an orthogonalized geometry, like in conventional cells, efficiency improvements may be realized in excitonic solar cells by making the junction normal to the substrate.<sup>43</sup> The critical dimension for these photovoltaic devices is the exciton diffusion length, which is typically much shorter, roughly 10 nm or less in polymers and up to a micrometer in high-quality small molecule films, than the minority carrier diffusion length in silicon.<sup>41,44</sup> This device structure maxi-

mizes the volume of the absorber material and/or interfacial area which contribute to charge generation while providing high-mobility channels through which these charges can be extracted.<sup>43</sup> Excitonic cells are promising due to their use of inexpensive organic materials such as dyes and polymers, and those that employ inorganic components generally use scalable solution syntheses. Features of the size, spacing, and aspect ratio required for efficient excitonic devices are currently accessible only through bottom-up synthetic approaches.

### 4.2.1. Polymer-Inorganic Hybrid Cells

Recent research on hybrid organic-inorganic solar cells has generated promising devices for inexpensive, large-scale solar energy conversion, in contrast to the high materials and production costs of the solid-state inorganic devices described above. While many semiconducting small molecules have superior electronic and optical properties, they are typically deposited by high-vacuum evaporation. Most relevant polymers, on the other hand, are soluble in organic solvents and can be cast onto any substrate in a variety of ways, including manufacturing-friendly roll-to-roll processes. In addition, these polymers generally have high absorption coefficients above  $10^5 \text{ cm}^{-1}$ .<sup>42</sup> Consequently, polymer-based solar cells can be much thinner, tens to hundreds of nanometers, than many of their inorganic counterparts. The short extinction length is fortunate because the exciton diffusion length in polymers is typically less than 10 nm<sup>44</sup> and the hole mobility in most polymeric devices is  $10^{-1}$ – $10^{-7} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ ,<sup>45</sup> as compared to about  $500 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$  in silicon. As a result, an optimal solar cell thickness arises from a balance of several parameters: the cell should be thick enough to maximize absorbance but thin enough maximize the exciton dissociation interface area and mini-



**Figure 5.** (a) Schematic illustration of typical band gap arrangement in a bulk heterojunction solar cell. (b–d) Diagrams of three types of donor–acceptor heterojunction photovoltaic cells: (b) polymer blend cell, (c) nanorod–polymer cell, and (d) ordered nanowire–polymer cell. In these drawings, electrons (filled circles) and holes (open circles) liberated from excitons (paired circles) split at the donor–acceptor interface must travel a convoluted path to reach the electrodes in either b or c, while the direct channels present in d result in short diffusion times and efficient charge collection.

mize the series resistance of the cell. Devices similar to conventional parallel multijunction cells have been produced to decouple these design parameters from film thickness.<sup>46,47</sup> The poor exciton and charge transport in polymer photovoltaic devices, though, suggests nanowire array anodes are the ideal structures to maximize their efficiency.<sup>43,45</sup>

Several groups have demonstrated prototype cells that utilize nanostructured blends of materials such as polymers, fullerene derivatives, and/or nanorods which avoid most of the costly fabrication associated with conventional Si and CdTe photovoltaics. Most hybrid devices comprise an interpenetrated network of electron donor and acceptor materials, called a bulk heterojunction, where the band offset at the extended interface induces dissociation of photogenerated excitons, as shown schematically in Figure 5a. The bulk heterojunctions used in organic,<sup>48</sup> nanorod–polymer,<sup>49</sup> and fullerene–polymer<sup>50,51</sup> photovoltaic cells are formed via phase separation during spin coating of mixtures of complementary donor and acceptor materials. These cells have much higher efficiencies than those based on single-component polymer films, but they are limited by inefficient charge transport due to the discontinuous percolation pathways, as shown schematically in Figure 5b and c. Ordered inorganic nanowire arrays can potentially address this issue, employing device morphologies like the one depicted in Figure 5d.

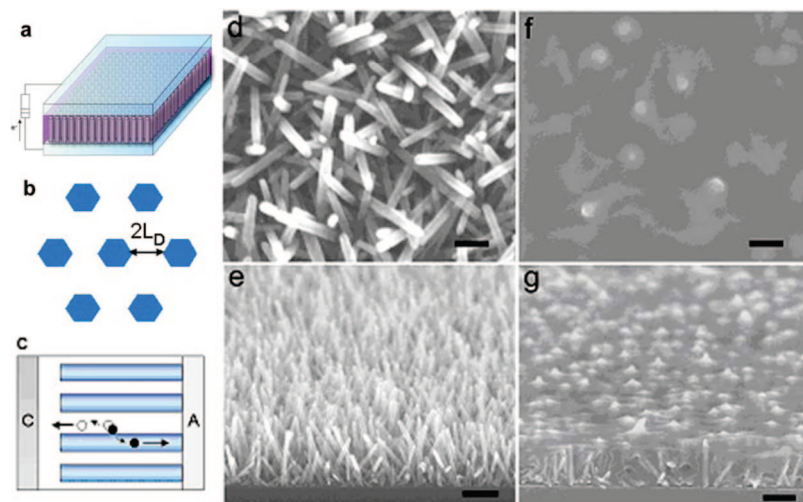
The external quantum efficiency of an excitonic solar cell is given by  $\eta_{EQE} = \eta_A \eta_{ED} \eta_{CC}$ , where  $\eta_A$  is the photon absorption efficiency,  $\eta_{ED}$  is the efficiency of exciton dissociation, and  $\eta_{CC}$  is the fraction of carriers collected by the electrodes of the device (Figure 5a). In a planar junction cell of total thickness  $L$  and average optical absorption length  $L_A$ , the absorption efficiency is approximately  $\eta_A = 1 - \exp(-L/L_A) > 50\%$ , while the charge collection efficiency is nearly 100%. However, since the exciton diffusion length,  $L_{ED}$ , is typically an order of magnitude smaller than  $L_A$ , the majority of photogenerated excitons are lost before reaching a donor–acceptor interface, resulting in a low  $\eta_{ED}$  and a low power conversion efficiency. Bulk heterojunctions in organic and inorganic–organic hybrid cells alleviate the limitation on film thickness of planar cells.<sup>51</sup> In a bulk heterojunction, the majority of excitons are generated within a distance  $L_{ED}$  of a dissociation site. Strong quenching of the photoluminescence in these films indicates that  $\eta_{ED}$  approaches unity,<sup>50</sup>

and conversion efficiencies as high as 5% have been reported.<sup>52,53</sup> Instead of a poor exciton dissociation rate, these films have poor charge transport characteristics, causing space–charge-limited currents and low efficiencies.<sup>54,55</sup> High-density nanowire arrays can improve charge conduction, especially in thick films for maximal absorption, while providing enough surface area to maintain a high  $\eta_{ED}$ .

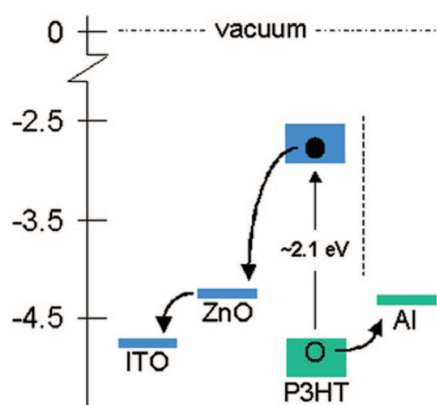
Bulk heterojunction cells with inorganic electron transporting materials have been fabricated from  $\text{TiO}_2$ <sup>56,57</sup> and  $\text{ZnO}$ <sup>58</sup> nanoparticle networks and ordered  $\text{TiO}_2$  mesoporous films.<sup>45</sup> In these devices the inorganic component acts as the electron conduction medium. In polymer cells using narrower band gap materials such as  $\text{CdSe}$ ,<sup>49,59,60</sup>  $\text{PbS}$ ,<sup>61</sup> and  $\text{PbSe}$ ,<sup>62</sup> the nanoparticle phase is both a complementary absorber and an electron transporter. By expanding the spectral range of absorption of the cell, these devices achieved nearly double the efficiency, up to 2.6%,<sup>63</sup> of those with wide band gap inorganic components. Charge separation and tunneling transport in polymer–inorganic hybrid devices<sup>45,55,57,64</sup> is similar to most excitonic and dye-sensitized solar cells (DSSCs), only nanoparticles in these cells do not have the advantage of charge screening and depletion that make DSSCs so efficient. Exciton dissociation in these devices is about 1000 times slower (1 ns)<sup>65</sup> than in the fastest polymer bulk heterojunctions<sup>66–68</sup> and is limited by potential barriers at the interface<sup>60,69</sup> and the nonideal density-of-states overlap between the polymer and inorganic phase. Electron transport and recombination rates, on the other hand, are extremely sensitive to the nanoparticle morphology, with directional and elongated nanorod structures significantly improving cell efficiencies.<sup>49,54,65,70</sup> For a review of other semiconductor nanocrystal- and carbon nanotube-polymer hybrid devices, see ref 71.

The field of inorganic nanowire–polymer hybrid solar cells is not as advanced as that of nanowire DSSCs, possibly due to the low overall efficiencies. Whereas DSSCs debuted at 7% power conversion efficiency in 1991,<sup>72</sup> even the best polymer bulk heterojunction cells barely reach 5%.<sup>52,53</sup> Polymer hybrid photovoltaics, however, promise to be one of the least expensive and easily manufactured classes of nanowire solar cells and therefore merit more attention. The first example of such devices was composed of electrodeposited CdTe in an anodized alumina template with poly(3-octylthiophene) as the hole-conducting polymer.<sup>73</sup> Though these polycrystalline nanowires likely did not have as low a series resistance as single-crystalline nanowires would have, they benefited from additional absorption in the CdTe and exhibited a respectable efficiency of about 1%. Cells have since been fabricated from polycrystalline transparent conductors such as  $\text{TiO}_2$ <sup>74,75</sup> and  $\text{ZnO}$ <sup>76</sup> using poly(3-hexylthiophene) (P3HT) as the absorber and show efficiencies of about 0.5%, a 2- to 4-fold improvement over planar control devices.

Single-crystalline nanowire (or nanorod, since due to the thin devices the aspect ratio of the inorganic phase is generally 10 or less) arrays of  $\text{ZnO}$ <sup>77–79</sup> and  $\text{InP}$ <sup>80</sup> have been recently used in polymer hybrid cells to speed electron conduction to the anode. The ZnO nanorods can be synthesized by a hydrothermal method on conducting glass or plastic substrates, yielding arrays of 20 nm diameter  $\times$  250 nm length rods with a packing density of roughly  $4 \times 10^{10} \text{ cm}^{-2}$ .<sup>81,82</sup> The surface area of nanorod substrate is about 10 times that of a flat surface of the same projected area, and the internanowire spacing is 10–20 nm on average, which



**Figure 6.** Diagrams of nanowire-based donor–acceptor heterojunction photovoltaic cells and SEM images of the organic–inorganic composite. (a–c) Diagrams for ordered nanowire–polymer cell. (d–g) SEM images. (d) Plan view image of a bare ZnO nanowire array. (e) Cross-section of the same array on a silicon wafer, tilted 15°. (f) Plan view after melt casting the P3HT film. (g) Cross-section of the nanowire–polymer composite. Scale bars in the top two images represent 100 nm and 200 nm in the bottom two images.



**Figure 7.** Simplified band diagram for the ZnO nanowire–polymer solar cell. Excitons are generated in the P3HT and split at the nanowire surface.

is comparable to the typical exciton diffusion length of the typical polymer absorber. The nanowire–polymer bulk heterojunctions are formed by spin coating regioregular P3HT (RR-P3HT) onto clean ZnO wire arrays. RR-P3HT is a conjugated, photostable conducting polymer with a large, for polymers, hole field-effect mobility of  $0.2 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ <sup>83</sup> and a relatively narrow band gap of 2.14 eV.<sup>84</sup> Annealing cast films to the melting point of P3HT followed by slow cooling allowed the polymer to fully penetrate the nanorod array with good wetting of the ZnO surface (Figure 6). The polymer volume should sufficiently cover the nanorods but not form too thick of a layer so as to impede charge conduction to the electrode.

The energetic and geometric parameters affecting the exciton dissociation efficiency,  $\eta_{\text{ED}}$ , are vital to the performance of hybrid cells. Exciton splitting at the ZnO/P3HT interface is both thermodynamically and kinetically favored.<sup>85</sup> A simplified band alignment picture is given in Figure 7.  $\eta_{\text{ED}}$  of these films was measured by comparing their quenched photoluminescence (PL), due to charge separation instead of radiative recombination, relative to the emission of neat P3HT films. Normalized PL spectra of filled arrays before and after the melting treatment indicate that nearly 30% of the excitons that decay radiatively in the planar film are split at the donor–acceptor interface in the nanowire

composite. X-ray diffraction patterns show that the polymer is crystalline, and absorption measurements show no evidence of the blue shift characteristic of poor RR-P3HT crystallization, suggesting that the polymer likely retains a high hole mobility.<sup>79</sup>

Greene and co-workers found that adding a  $\sim 5$  nm polycrystalline  $\text{TiO}_2$  shell improved the efficiency of their devices 5-fold, primarily due to increases in  $V_{\text{OC}}$  and  $FF$ .<sup>79</sup> This effect was ascribed to poor interface charge separation between P3HT and ZnO. Plank and co-workers observed similar results and came to the same conclusion with ZnO–MgO core–shell structures in cells incorporating an absorbing dye at the interface.<sup>86</sup> Neither of these materials, however, produced devices with efficiencies above 0.4%, mostly due to the small volume of polymer between neighboring nanowires. Polymer confinement can also lead to conformational changes that are detrimental to exciton diffusion and hole transport.<sup>87</sup> The independence of  $J_{\text{SC}}$  on polymer thickness in the cells of Greene et al. supports this result and suggests that inefficient exciton dissociation, rather than charge transport, is responsible for the poor device performance.

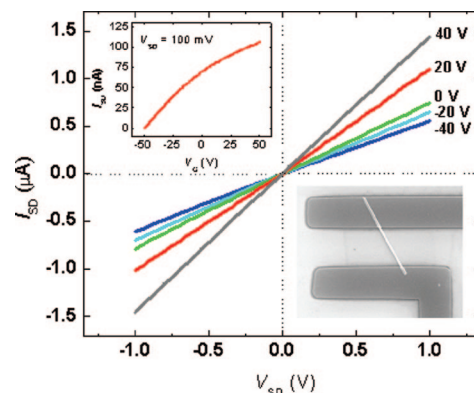
The highest efficiencies for nanowire–polymer hybrid cells have been measured in devices using bulk heterojunction polymer films. Using ZnO nanowires predominantly as electron conductors, rather than an interface for charge separation, these cells operated at between 2% and 4% efficiency.<sup>88–90</sup> With films of P3HT:PCBM, Takanezawa and co-workers demonstrated higher efficiencies corresponding to longer nanowires, suggesting that the ZnO behaves as a better charge conductor than the PCBM. In this case, the majority of the exciton dissociation occurred at the P3HT–PCBM interface, which is known to separate charge efficiently, and electrons were subsequently shuttled from the PCBM to the nanowires.<sup>89</sup> The inorganic phase may also be responsible for exciting extra charge carriers, which can contribute to the device  $J_{\text{SC}}$ . This strategy seems to be a promising way to decouple the poor charge separation properties of the nanowire–polymer interface from the superior charge conduction of the nanowires and may be the ideal route to efficient and inexpensive inorganic–polymer hybrid solar cells.

#### 4.2.2. Dye-Sensitized Solar Cells

Another example of an excitonic cell is the dye-sensitized solar cell (DSSC),<sup>40,72,91</sup> in which light-absorbing dye molecules decorate high surface area films of an inorganic, electron-conducting phase. The DSSC is currently the most efficient<sup>92,93</sup> and stable<sup>94</sup> excitonic cell. Light absorption in these cells is limited to the dye monolayer, which subsequently oxidizes a liquid electrolyte and transfers electrons to the inorganic phase. DSSC anodes are typically constructed of nanoparticle films several micrometers thick or more. These films are typically composed of TiO<sub>2</sub>,<sup>95</sup> but also SnO<sub>2</sub><sup>96</sup> or ZnO,<sup>97</sup> nanoparticles to achieve high surface area supports for the dye monolayer. The high dye concentration yields optically thick anodes with large light absorption in the 400–800 nm region, where a large portion of solar energy is incident. Upon charge separation, electrons conduct by diffusive tunneling, “hopping”, transport through the percolation network of nanoparticles.<sup>40,98</sup> The films must be thick to maximize the path length of incident light and hence the absorption by the dye, which is particularly inefficient in the red and near IR. Inefficient electron transport, however, prevents cells from being thicker due to low electron diffusion constants and recombination losses.<sup>99–104</sup>

Compared to single-crystalline ZnO and TiO<sub>2</sub>, nanoparticle films of these materials exhibit significantly slower charge transport. Time-resolved photocurrent and photovoltage measurements<sup>99,105</sup> and modeling efforts<sup>106,107</sup> indicate that electron transport in wet, illuminated nanoparticle networks occurs by a trap-limited diffusion process. Drift transport, i.e., in response to an electric field, is prevented in DSSCs by ions in the electrolyte that screen macroscopic electric fields.<sup>108</sup> The electron diffusion coefficients are several orders of magnitude lower in nanoparticle devices ( $D < 10^{-4}$  cm<sup>2</sup> s<sup>-1</sup>)<sup>109–113</sup> as compared to TiO<sub>2</sub><sup>114</sup> and ZnO<sup>115</sup> single crystals. Remarkably, the charge collection efficiency of these films is high due to the slow kinetics of the back reaction of injected electrons with the electrolyte, typically I<sub>3</sub><sup>-</sup> reduction at the anode surface.<sup>108</sup> The electron diffusion lengths in these devices are accordingly long, up to several tens of micrometers at low illumination intensities.<sup>94,116,117</sup> However, current research efforts on DSSC improvements focus on the development of new dyes<sup>118–121</sup> and electrolytes,<sup>122,123</sup> thus changing the kinetics of the forward and reverse redox reactions.<sup>124</sup> In these and other cases where surface recombination becomes significant, such as in polymer–inorganic hybrid cells, the low electron diffusion coefficient values become significantly more detrimental to device performance.

Single-crystalline ZnO nanowires, on the other hand, are excellent conductors. Electrical measurements of dry arrays on conductive glass give linear  $I$ – $V$  traces that indicate an ohmic contact between the nanowires and the substrate. The transport properties of individual nanowires were measured using field-effect devices fabricated by electron beam lithography, as shown in Figure 8. The resistivity values of as-grown ZnO nanowires ranged from 0.3 to 2.0 Ω cm, falling on the conductive end of the spectrum for nominally undoped ZnO.<sup>125</sup> The carrier concentration was about  $5 \times 10^{18}$  cm<sup>-3</sup>, and a field-effect mobility of 1–5 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> was calculated for typical devices from the transconductance plots. Using the Einstein relation,  $D = kT\mu/e$ , where  $D$  is the diffusion constant,  $kT$  is the thermal energy,  $\mu$  is the mobility, and  $e$  is the elementary charge, the electron diffusion coefficient for individual dry nanowires was calculated to be 0.05–0.5 cm<sup>2</sup> s<sup>-1</sup>. These diffusion coef-



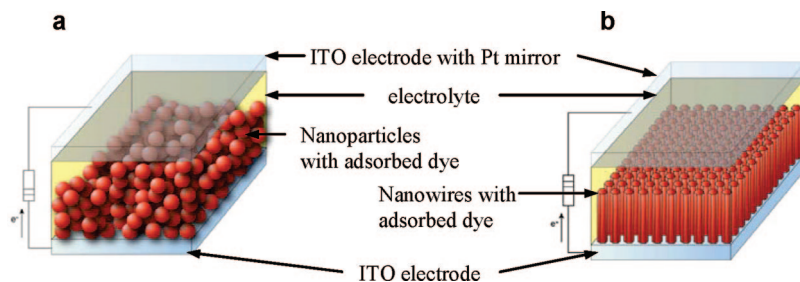
**Figure 8.** Single ZnO nanowire electrical characteristics.  $I$ – $V$  curves at various gate biases for a nanowire with a diameter of 75 nm, showing n-type behavior and a zero-gate resistivity of 0.65 Ω cm. (Right inset) SEM image of a nanowire device. (Left inset) Transfer plot ( $I_{SD}$  vs  $V_G$ ) of a nanowire FET, taken at  $V_{SD} = 100$  mV. The ON–OFF ratio in this case is  $10^5$  at  $\pm 50$  V. (Reprinted with permission from ref 127. Copyright 2005 Nature.)

ficients are several orders of magnitude greater than those found in nanoparticle films and indicate that ZnO nanowires are superior electron conductors.

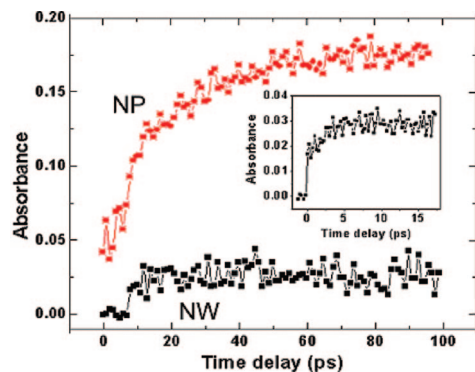
Structurally, nanowire array-based DSSCs are like nanoparticle DSSCs with the particles assembled into columns and without grain boundaries, thus forming direct conduction channels, a highway for electrons. However, there are fundamental differences in the physics which govern the nanowire device behavior. First, unlike the mesoporous nanoparticle films, the mean nanowire diameter is thick enough to support a depletion layer near the surface. This potential barrier can provide an energetic driving force for exciton dissociation at the interface between the ZnO and the dye, making charge injection more efficient. Also, band bending sweeps electrons away from the surface, potentially reducing the rate of recombination. Second, because electrons in the nanowires are not isotropically screened by counterions in the electrolyte, these DSSCs can sustain an internal electric field along the axis of the nanowires. As a result, electrons injected into the nanowires drift toward the substrate electrode, down the chemical potential gradient. Furthermore, electron mobility in the nanowire is larger than that in the particle films due to their directional and uninterrupted conduction channel, as opposed to the tortuous percolation network and grain boundaries of the nanoparticle films. This directed transport is expected to increase the electron diffusion constant, thus improving the efficiency of charge collection and enabling the production of optically thick cells which absorb more incident light. The schematic comparison of such devices is shown in Figure 9.

Nanowire-based DSSCs were first realized with vertically oriented, single-crystalline ZnO nanowire arrays,<sup>81,82,126–128</sup> with efficiencies from 0.5%<sup>128</sup> to 1.5%.<sup>127</sup> Solar cells were fabricated from nanowire arrays of various lengths and tested under AM 1.5 illumination. Longer nanowires performed better, with record cells having  $J_{SC} = 6.0$  mA cm<sup>-2</sup>,  $V_{OC} = 0.68$  V,  $FF = 0.45$ , and a power conversion efficiency of 1.5%. The external quantum efficiency of these cells peaks at 45% near the absorption maximum of the dye.<sup>127</sup> The open-circuit voltage is about 0.1 V better in the nanowire cell, likely a result of lower recombination losses. The low short-circuit current and quantum efficiency, as compared with the best TiO<sub>2</sub> nanoparticle devices, are due to the smaller total surface area of the nanowire array. The nanoparticles, having a





**Figure 9.** Schematic representations of a DSSC. (a) Traditional cell (nanoparticle film electrode). (b) Nanowire DSSCs.

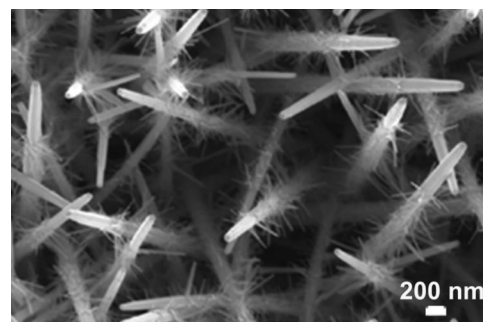


**Figure 10.** Transient mid-IR absorption traces of dye-sensitized ZnO nanowire (NW) and nanoparticle (NP) films pumped at 400 nm. The large difference in injection amplitudes is due to the much larger dye loading of the particle film. Injection in wires is complete after  $\sim 5$  ps but continues out to  $\sim 100$  ps in the particle case. A high-resolution trace (inset) shows the ultrafast step ( $<250$  fs) and  $\sim 3$  ps rise time for a nanowire sample. (Reprinted with permission from ref 127. Copyright 2005 Nature.)

greater surface to volume ratio, are able to adsorb more dye per unit thickness of the cell and thus absorb more light and collect more charge.

Despite the lower overall efficiencies, these model ZnO nanowire devices demonstrate promising fundamental improvements over the mesoporous polycrystalline films. Specifically, using the same dye, loading conditions, and electrolyte, ZnO nanowire devices showed significantly higher  $J_{SC}$  than nanoparticle films with the same dye-adsorbed surface area. Law and co-workers found that, consistent with studies on TiO<sub>2</sub> nanoparticle films,<sup>129</sup> larger ZnO nanoparticles displayed higher  $J_{SC}$  values in thin devices, presumably as a result of the higher diffusion constant in the films. In addition, the  $J_{SC}$  values of the ZnO nanowire devices were still higher, suggesting a lower series resistance within the cell.<sup>130</sup> Devices composed of TiO<sub>2</sub> nanoparticle films with the same dye-adsorbed surface area had the highest  $J_{SC}$  values. Using femtosecond transient absorption spectroscopy, Law and co-workers also found that the kinetics of charge injection from the dye excited state into the nanowires were significantly faster than across the dye–nanoparticle interface. The difference in the charge transfer rates, shown in Figure 10, may be the result of improved electron injection through the well-defined facets of the nanowire–dye interface as compared to the multitude of crystal facets presented at the nanoparticle surfaces.<sup>127</sup> Within the ZnO material system, the nanowire cell geometry clearly exhibits improved charge injection and transport characteristics, but the overall efficiency still lags behind TiO<sub>2</sub> anodes.

For nanowire-based DSSCs, the most significant limiting factor is almost always the small specific surface area (or



**Figure 11.** SEM image of branched ZnO nanowires.

roughness factor). Several groups have attempted to augment the area available for dye absorption in the nanowire-based cells. Tan and Wu<sup>131</sup> found that mixing high aspect ratio TiO<sub>2</sub> nanowires with TiO<sub>2</sub> nanoparticles in a disordered DSSC anode film improved the efficiency of the device. The DSSCs exhibited a maximum in their performance, at 20% nanowire content by weight, due to a balance between dye loading surface area and charge transport. The optimum composite mixture produced cells with an average efficiency of 8.6% as compared to 6.7% for the pure nanoparticle film. Similarly, filling the space between ZnO nanowires in vertically aligned arrays with ZnO nanoparticles improved cell performance, increasing the total surface area of the device.<sup>132,133</sup> Ku and co-workers found that  $J_{SC}$  increased with the addition of the nanoparticles, and the efficiency of the cell improved 3-fold, consistent with Tan and Wu's results.<sup>132</sup> Furthermore, the effective electron diffusion constant was about 10 times smaller than in the pure nanowire device, indicating that much of the additional photocurrent still percolated through the nanoparticle network to either the nanowire or the substrate. A subsequent study showed that improving the nanowire–nanoparticle interface using a chemical bath deposition technique further enhanced DSSC performance.<sup>134</sup> The cell efficiency in this case was five times greater than the pure nanowire device. Other groups have also demonstrated improved efficiencies in cells composed of ZnO nanowires grown off of the existing nanowire array.<sup>135,136</sup> This tree-like anode morphology, as shown in Figure 11, serves to fill the voids between nanowires with single-crystalline charge conduction pathways. If the density of these branched structures can be maximized, they should exhibit optimal solar cell performance.

The discrepancy between the properties of ZnO and TiO<sub>2</sub> as DSSC anode materials is the subject of ongoing investigation, but TiO<sub>2</sub> generally performs better with the dyes and electrolytes that have been studied. The poorer performance of the ZnO-based devices may be a result of surface chemical reactions,<sup>137</sup> the formation of dye aggregates,<sup>138,139</sup> surface trap states,<sup>140,141</sup> ZnO seed layer processing,<sup>142,143</sup> or slow charge injection from commonly used dyes.<sup>105,144,145</sup> To this

end, several groups have employed radial (core–shell) nanowire heterostructures to exploit the charge transfer characteristics and surface stability of  $\text{TiO}_2$  while retaining the fast electron transport of single-crystalline nanowires. Law and co-workers<sup>146</sup> synthesized  $\text{ZnO}$  nanowire arrays coated by atomic layer deposition (ALD) with  $\text{TiO}_2$  films of varying thickness. The difference between the Fermi levels of the  $\text{ZnO}$  core and  $\text{TiO}_2$  shell creates a weak type II band offset at the interface, which provides a potential barrier to charge recombination. Overall, the core–shell devices exhibited a 2-fold improvement in efficiency (0.85–2.1%) that was attributed to a combination of improved charge injection and reduced recombination current.<sup>147</sup> Similar devices were constructed using  $\text{SnO}_2$  nanowires as the conducting core with  $\text{TiO}_2$  nanoparticle<sup>148</sup> and ALD-deposited<sup>148,149</sup> films. The nanoparticle-coated arrays gave the best device efficiencies, about 4%, and recombination time constants, as calculated from  $V_{\text{OC}}$  decay curves, were about 100-fold longer for the nanowire arrays than the nanoparticle films.

Although  $\text{ZnO}$  appears to be an inferior material to  $\text{TiO}_2$  as the DSSC anode, model  $\text{TiO}_2$  nanowire array structures have proved a significant synthetic challenge. Various techniques have been developed to reduce the resistance to electron transport between particles, such as growth of aligned polycrystalline nanotubes,<sup>150–152</sup> high-temperature sintering,<sup>153</sup> ordering of the mesoporous structure,<sup>154,155</sup> and synthesis of particle films by oriented attachment.<sup>156</sup> All these methods strive to minimize electronic defects at the grain boundaries, but none will likely demonstrate as great an improvement as high surface area single crystals. The decreased density of trap states leading to faster electron transport improves overall cell performance only when coupled with the energetic barrier to charge recombination provided by the nanowire structure.<sup>100,157</sup> Recently, several groups have achieved high-density, single-crystalline, vertically aligned  $\text{TiO}_2$  nanowire arrays.<sup>158,159</sup> The efficiency of DSSCs fabricated from these arrays is encouraging, about 5%, though the nanowire density is too low such that the surface area available for dye adsorption suffers.<sup>158</sup> With further synthetic optimization, or perhaps combination with nanoparticles in a composite film, these  $\text{TiO}_2$  nanowire anodes are promising candidates for high-efficiency DSSCs.

Analogous photovoltaic devices based on the principles of DSSCs have also been fabricated on  $\text{ZnO}$  nanowire substrates. Leschkes and co-workers used  $\text{CdSe}$  quantum dots, instead of an absorbing dye, to sensitize arrays of  $\text{ZnO}$  nanowires immersed in an  $\text{I}_2/\text{I}_3^-$  redox couple solution. Though the cell efficiency was low (0.4%), it had a reasonable  $V_{\text{OC}}$  of 0.6 V and an internal quantum efficiency of 50–60% over the wavelengths corresponding to the first excitonic transition of the nanoparticles.<sup>160</sup> Different nanoparticle sizes or compositions can be used to tune the absorption range of these cells independent of surface chemistry, unlike the sensitizing dyes. All inorganic-sensitized solar cells were fabricated on a  $\text{ZnO}$  nanowire array, with a  $\text{CdSe}$  or  $\text{CdTe}$  absorber layer, and  $\text{CuSCN}$  transparent hole-conducting layer deposited from a chemical bath solution.<sup>161,162</sup> The cells with the  $\text{CdSe}$  absorber layer performed better, with a 2.3% power conversion efficiency at low illumination intensities (36  $\text{mW cm}^{-2}$ ), but both suffered from significant recombination losses.

The energy conversion efficiencies of current nanowire-based excitonic cells are not better than the best nanoparticle-

based DSSCs, but the studies discussed above show that their electron transport properties are superior. Nanoparticle films have at least a 5-fold larger surface area than the best nanowire arrays, resulting in more dye loading per area of the cell and hence more absorption within the particle films. Longer or thinner nanowire arrays with comparable surface areas are a synthetic challenge, but they should improve both light absorption and charge transport within the anode.

Inorganic nanowires represent an ideal charge transport medium for nanostructured solar cells and display promising improvements over planar and nanoparticle-based devices. Further efficiency improvements may be realized in several areas of research. (1) Better synthetic control. Ideal electrode structures have not been realized, in terms of both their surface area and length as well as their band gap. (2) Improved device architectures. The performance of nanorod–polymer devices has been disappointing thus far. Incorporating different absorbers, such as small molecules or nanocrystals, and hole conductors, such as inorganic layers, in a variety of device schemes would provide comparative data to help understand the factors that determine efficiency. Atomic layer deposition is a particularly useful technique in this regard. (3) Time-dependent transport experiments. Time- and frequency-domain photocurrent and voltage experiments may shed light on the charge separation and transport properties of nanowire-based solar cell. (4) Device modeling. Three-dimensional models of nanostructured solar cells would help explain the role of electric fields and the microscopic dynamics of recombination in these cells.

## 5. Nanowires for Electrochemical Energy Storage

The storage of energy through electrochemical reactions is a crucial technology for portable power needs and load leveling of many alternative and conventional power sources. The proliferation of personal electronics and commercialization of electric and hybrid electric vehicles has popularized the need for rechargeable, portable power sources. High-capacity energy storage is also necessary for the widespread use of intermittent alternative energy resources, most notably from the sun and wind. For these technologies to contribute significantly to the global demand for electricity, they require efficient methods to store excess energy when it is abundant, during the day, for example, or when the wind blows, and to release it as needed. Such storage would also improve the efficiency of conventional power generation by leveling the peaks and valleys of daily energy demand.<sup>163,164</sup> Batteries fulfill these requirements by providing a direct route to the conversion of electrical to chemical energy.

The basis for this energy conversion is the simultaneous shuttling of electrons to and from electrodes via complementary chemical reactions. During discharge, electrons are delivered from the cathode to the anode through an electrolyte medium by spontaneous reduction and oxidation, respectively, of chemical species at the electrodes. Electrons supply power by flowing from the anode back to the cathode through an external load. Applying an opposing voltage across the cell reverses the reactions at the electrodes, thus recharging the battery capacity. Fuel cells are similar to batteries in that spontaneous redox reactions at the electrodes produce a voltage to push electrons across an external load. The reactants for these reactions, however, are generally not recycled within the cell but rather supplied to the electrodes from an external source as liquids or gases. In a hydrogen fuel cell, for example, hydrogen and oxygen gas are oxidized

and reduced at the anode and cathode, respectively, forming water, which is subsequently removed. Similarly, photoelectrochemical (PEC) cells use a photovoltage, instead of an externally applied voltage, to drive redox reactions at the electrode surfaces in a process analogous to battery charging. The DSSC devices discussed above are in fact PEC cells where the chemical fuel, the oxidized electrolyte, is converted back to an electrical potential internally. The factors affecting PEC efficiencies are therefore similar to those in the DSSC section above and will not be covered in further detail. Suffice it to say that nanowires have been used in PEC devices with some demonstrated advantages over planar and nanoparticle-based electrodes.<sup>152,165–174</sup>

Lithium ion batteries, which use lithium cations to transport charge between electrodes, are promising for rechargeable chemical energy storage due to the fast mobility and high energy density of lithium ions. Lithium also has a large negative reduction potential ( $E^0 = -3.05$  V) which generates a high-voltage output. Only hydrogen would offer better storage and transport properties if not for the difficulty of storing large weight fractions of it.<sup>163</sup> The energy stored in these batteries is a result of the difference in the redox potentials of lithium insertion into the two electrodes. As a result, the battery capacity depends on the weight or volume fraction of lithium that each electrode can hold, and the rate of charging and discharging depends on the electrical resistance of the electrodes and the rate of lithium diffusion in and out of the electrode materials.<sup>164</sup>

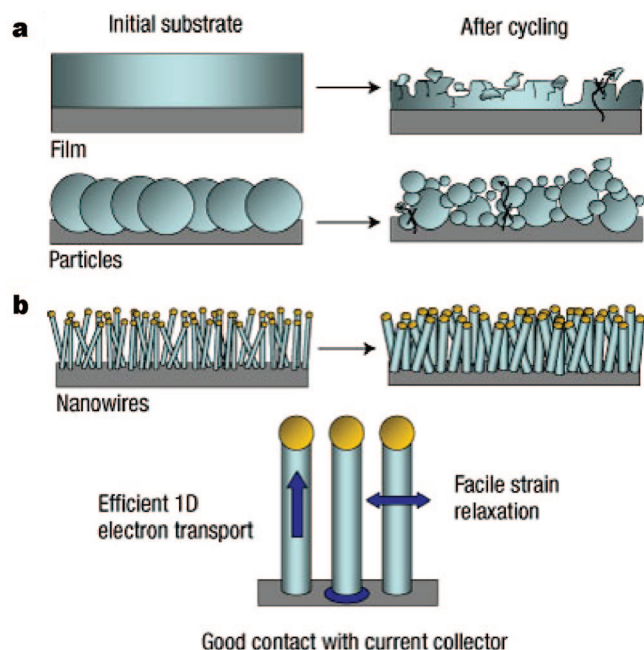
Nanoparticles have been employed as electrode materials to improve cell properties in several ways. First, small diameter particles have large surface-to-volume ratios and short lithium diffusion lengths. The diffusion time varies as the square of the length, so the greater available surface area and reduction of particle sizes from micro- to nanometers result in orders of magnitude increases in lithium insertion and discharging kinetics. Nam and co-workers, for example, used viral capsid templates to nucleate monodisperse  $\text{Co}_3\text{O}_4$  and  $\text{Au-Co}_3\text{O}_4$  composite nanoparticles by a low-temperature synthesis, which exhibited superior lithium storage and discharge properties normally found only in higher temperature syntheses ( $\sim 500$  °C).<sup>175</sup> Second, nanoparticles may sustain more damaging structural changes than their bulk material counterparts. Particles below a critical size may resist phase transformations<sup>176</sup> or undergo transitions through and amorphous intermediate<sup>177,178</sup> and prevent large lattice strains resulting from phase coexistence. Furthermore, nanoparticles resist cracking due to strain relaxation at the surface<sup>179,180</sup> and mechanical considerations.<sup>181</sup> Finally, the high surface-to-volume ratio of the nanoparticle electrodes increases their reactivity significantly, which allows for different reaction mechanisms than are observed at bulk material surfaces. For example, instead of incorporating into the lattice of several metal–oxide electrode materials, lithium reversibly reduces the electrodes to metal particles surrounded by  $\text{Li}_x\text{O}$ .<sup>182</sup>

As with PEC or solar cells, nanoparticle battery electrodes suffer from poor charge transport. While some materials with high lithium storage capacities are inherently poor conductors, the cyclic strain fluctuations of the electrodes upon lithium insertion and removal causes greater separation between nanoparticles and impedes charge percolation to and from the current collectors. The former can be addressed with impurity doping of the electrode material, though these defects may lead to deterioration of the electrochemical

properties of the electrode. The latter problem is often remedied by encapsulating the nanoparticles in a conductive carbon matrix, thus reducing the series resistance to the current collector.<sup>176</sup> Carbon, however, even in the form of graphite has a limited capacity for intercalating lithium, such that a pure graphite electrode can reach a specific capacity of about  $350 \text{ mAh g}^{-1}$ .<sup>164,183</sup> As a result, the specific capacity of the electrode suffers. In materials such as silicon and tin the effects of volume expansion and contraction (up to 400% for  $\text{Li}_{4.4}\text{Si}$ ) are exacerbated due to their especially high capacity for lithium.<sup>184</sup> For a comprehensive review of silicon nanoparticle-based anodes, see ref 185. The high lithium content of these alloys though is precisely the property that makes these materials so attractive for battery anodes.

Due to the lack of a conductive matrix, nanometer-scale thin films of amorphous silicon exhibit much greater specific capacities than nanoparticle electrodes and longer cycle lives than bulk silicon.<sup>185</sup> These films routinely store  $>3000$ – $4000 \text{ mAh g}^{-1}$  due to their large surface-to-volume ratio and exhibit excellent capacity retention up to several hundred cycles. For up to 200 cycles, Ohara and co-workers found that thinner amorphous silicon films had a greater specific capacity, 3700, 2900, and 2900  $\text{mAh g}^{-1}$  for 50, 150, and 440 nm films, and that the capacity loss per cycle increased with thickness.<sup>186,187</sup> Their results follow this trend up to thicker films of several micrometers.<sup>188</sup> The researchers concluded that the improved charge retention in the thinner films was a result of less cracking and degradation of the electrical contact between the thinner films and their substrates. The thinner the films, the better they were able to withstand the expansion and contraction cycles under lithium loading. Although these results are fundamentally significant, the application of thin films in commercial battery technologies seems limited since the surface area accessible to the electrolyte is limited by the area of the substrate.

Nanowire anodes, in contrast to nanoparticle and thin film materials, should in principle maximize the electrode surface area while maintaining good electrical connections to the current collector (Figure 12). Indeed, Gao and co-workers adopted this strategy for VLS-grown silicon and germanium nanowires but with disappointing capacities of about  $800$ – $1500 \text{ mAh g}^{-1}$  and no cycling experiments.<sup>189</sup> Chan and co-workers discovered they could achieve near theoretical specific capacity ( $4200 \text{ mAh g}^{-1}$ ) for VLS-grown silicon nanowire anodes on a stainless steel current collector.<sup>190</sup> The anode capacity fell immediately following the first cycle but remained constant at about  $3000 \text{ mAh g}^{-1}$  for 10 cycles at a C/20 rate (i.e., discharging 1/20th the cell capacity per hour). Another device exhibited a capacity of roughly  $3500 \text{ mAh g}^{-1}$  for 20 cycles at a C/5 rate. TEM images show that the initially single-crystalline nanowires became amorphous after lithium insertion and reverted to amorphous silicon after the first discharge cycle. Ge nanowire anodes synthesized in a similar manner exhibited analogous results, albeit at a lower specific capacity of  $1141 \text{ mAh g}^{-1}$  over 20 cycles at a C/20 rate.<sup>191</sup> With another variation, Cui and co-workers investigated the cycling behavior of crystalline core–amorphous shell silicon nanowire anodes also grown by the VLS mechanism.<sup>192</sup> Due to differences in the reduction potential of lithium in amorphous versus crystalline silicon, the researchers proposed that the core could serve as a mechanical support and provide high electrical conductivity along the entire length of the nanowire array. Indeed, the lithium reduction peaks were distinguishable in electrochemical



**Figure 12.** Schematic representation of (a) the failure mechanisms of thin film and nanoparticle silicon lithium ion battery anodes and (b) the expected mechanisms of cycling stability of silicon nanowire anodes. (Reprinted with permission from ref 190. Copyright 2008 Nature.)

potential spectroscopy scans. Although cycling below the crystalline silicon reduction potential for lithium produced a large initial specific capacity,  $2400 \text{ mAh g}^{-1}$ , the capacity dropped about 20% over 30 cycles. By cycling the cells with a lower voltage limit above this potential, the anodes exhibited lower capacities, about  $800\text{--}1000 \text{ mAh g}^{-1}$ , but lost less than 10% of their capacity over 100 cycles, making them promising candidates for practical applications.

Other nanowire-based anodes have been studied in a variety of material systems and also demonstrate improved properties as compared to nanoparticle and bulk materials. Mesoporous materials, essentially inverse nanowire structures, composed of carbon and polycrystalline silicon-carbon composites, akin to encapsulated nanoparticle systems, have demonstrated higher specific capacities and rate capabilities (the capacity upon discharge at various rates) than films or bulk electrodes.<sup>193–195</sup> These improvements are likely a result of the high electrode surface area, and the short lithium diffusion lengths improve the rate at which charge can be extracted from the electrode.  $\text{SnO}_2$  nanowire anodes have also shown improved specific capacity for lithium insertion as compared to nanoparticle anodes and retain these high capacities even at fast discharge rates ( $>700 \text{ mAh g}^{-1}$  at 8C).<sup>196,197</sup> Similar capacities were measured for mesoporous  $\text{FeC}_2\text{O}_4$  nanoribbons.<sup>198</sup> Titania nanowires, synthesized as  $\text{TiO}_2\text{-B}$ , a polymorph of titania with a more open lattice structure than anatase and rutile,<sup>199</sup> exhibited better lithium capacity than nanoparticles in both liquid and polymer electrolyte cells.<sup>200,201</sup>  $\text{TiO}_2\text{-B}$  also has the added benefit of reducing lithium at a much higher potential than lithium metal, thus preventing growth of elemental lithium dendrites, which can short cells and lead to excessive heating and explosion, even under fast charging conditions.<sup>201</sup> Single- and polycrystalline  $\text{Co}_3\text{O}_4$  nanowires have also been implemented as battery anodes with improved capacity and rate capabilities.<sup>175,202–204</sup> Analogous arrays of vertically aligned, electrodeposited Cu nanowires have been used as supports

for polycrystalline  $\text{Fe}_3\text{O}_4$  shells, which produced cells with surprisingly persistent specific capacities of  $800\text{--}900 \text{ mAh g}^{-1}$  over 50 cycles and high rate capacities, retaining 75% of the discharge capacity of the C/32 rate at a 8C rate.<sup>205</sup>

Cathodes composed of nanowire-based materials show improved properties as well, the main difference between these and anode materials being the voltage at which they reduce lithium. The measured specific capacities and rate capability of  $\text{LiMn}_2\text{O}_4$  nanowire and nanotube cathodes is greater than comparable nanoparticle electrodes ( $\sim 100 \text{ mAh g}^{-1}$  at slow discharge rates).<sup>206,207</sup> The discharge capacity at various rates was found to improve with decreasing nanotube wall thickness, consistent with the shorter lithium diffusion lengths.<sup>206</sup> Furthermore, Kim and co-workers found that the nanowires retained good electronic connections to the current collector through many charge/discharge cycles. Consequently, less conductive carbon was required to preserve conductivity through the electrode, effectively increasing the specific capacity of the nanowire array to twice that of commercial nanoparticle cathodes.<sup>207</sup> Similar improvements were observed in  $\text{C@Au@V}_2\text{O}_5$  core-shell-shell nanorod structures synthesized by a template method.<sup>208</sup> The crystallinity of  $\text{V}_2\text{O}_5$  nanorods also aids Li intercalation, as Takahashi and co-workers discovered that nanorods grown by electrochemical deposition had 5 times greater current density than substrates with poorer crystallinity synthesized by a sol-gel method.<sup>209,210</sup>  $\text{V}_2\text{O}_5 \cdot n\text{H}_2\text{O}$  was found to be even more active for lithium incorporation, and  $\text{Ni@V}_2\text{O}_5 \cdot n\text{H}_2\text{O}$  core-shell nanowire arrays showed 10 times greater current density than the crystalline nanorods.<sup>211</sup> Somewhat conflictingly, another study found that amorphous  $\text{V}_2\text{O}_5$  nanotube arrays also showed promising specific capacities of about  $300 \text{ mAh g}^{-1}$  but degraded by cycling to  $160 \text{ mAh g}^{-1}$ .<sup>212</sup> In addition to short diffusion lengths and large surface areas, the diffusion constant of lithium in  $\text{V}_2\text{O}_5$  is sensitive to the material dimensions. Chan and co-workers determined that the diffusion of lithium in  $\text{V}_2\text{O}_5$  was up to 1000 times faster in nanoribbons than in bulk and, moreover, that  $\text{Li}_3\text{V}_2\text{O}_5$  reverted to crystalline  $\text{V}_2\text{O}_5$  upon removal of lithium from the electrode.<sup>213</sup> The increased surface reactivity of the nanoribbons is presumably responsible for facilitating the phase transformations associated with lithium insertion and extraction. Of particular interest is the energy storage mechanism of these  $\text{V}_2\text{O}_5$  nanostructured materials. Their energy density resembles that of typical batteries, while their power density is comparable to that of capacitors, another device for which nanowire electrodes are receiving increasing attention. These nanostructures clearly store chemical energy in different ways than conventional materials and represent an intriguing avenue for future research.

In short, nanowire-based materials have demonstrated significant improvements over conventional lithium battery electrode materials with higher specific capacities, rate capabilities, better cycling performance, and new phase transformation behavior. Although it is unlikely that lithium secondary batteries will be used for all applications, especially those requiring very high rate capabilities, the design principles elucidated by the study of nanowire electrodes in these cells may help develop significantly improved electrodes in other rechargeable battery systems. For example, transportation accounts for over 60% of the petroleum energy consumption in the United States and about 20% of total energy consumption worldwide.<sup>214</sup> Lithium ion batteries are currently used to power electric and hybrid vehicles and

could potentially provide inroads for alternative energy into these markets in place of relatively inefficient combustion engines. However, due to current limitations on the properties of electrolytes, separators, and charge collectors, specific capacities above 1000 mAh g<sup>-1</sup> or energy densities above 1 Ah cm<sup>-3</sup> are unlikely to significantly improve overall battery performance.<sup>185</sup> Looking forward, the most important advances will likely come from discovering more inexpensive and abundant materials that can achieve dense lithium storage capacities and long cycle lifetimes by altering the electrode morphology. In this sense, the promise of scalable syntheses<sup>215</sup> of nanowire-based anodes with long cycle lives is encouraging for future energy storage applications.

## 6. Nanowires for Thermoelectric Applications

Thermoelectric materials convert heat to electricity. When placed in a temperature gradient, these materials generate an electrical potential that can be used to power an external load. Conversely, passing a current through a thermoelectric material will establish a temperature gradient across the material, shunting heat from one side to the other. Consequently, depending on their temperature range of maximum efficiency, these materials may be used for either solid-state power generation or cooling. Thermoelectric modules may also be used as power cogenerators by salvaging waste heat from other power generators such as combustion engines. Approximately 90% of the world's power (~10<sup>13</sup> W or 10 TW) is generated by heat engines that operate at 30–40% efficiency, such that roughly 15 TW of heat is lost to the environment. Thermoelectric materials may potentially convert part of this low-grade waste heat to electricity, which could result in significant fuel savings and a reduction in carbon emissions.

In a temperature gradient, charge carriers on the hot side of a material occupy higher energy electronic states than those on the cold side. These hot electrons (or holes) diffuse to the cold side, where the density of available states is greater, until the opposing electric field is sufficient to stop the flow of charge. The efficiency at which thermoelectric materials convert heat to electricity depends on the thermoelectric figure of merit ( $ZT$ ), which is defined as  $ZT = S^2T/\rho k$ , where  $S$ ,  $\rho$ ,  $k$ , and  $T$  are the Seebeck coefficient, electrical resistivity, thermal conductivity, and absolute temperature, respectively. Conceptually, ideal thermoelectric materials must have a low  $k$ , to maintain large temperature gradients, and a low  $\rho$ , to minimize Joule heating and maximize the available charge carriers contributing to the thermoelectric effect.  $S$  depends on the electronic band structure of the material near the Fermi level and varies with the change in carrier density per degree Kelvin. The total efficiency of a thermoelectric material is a function of the Carnot efficiency (the thermodynamic maximum efficiency) and  $ZT$  according to

$$\eta = \eta_C \frac{\sqrt{1 + ZT} - 1}{\sqrt{1 + ZT} + T_c/T_h}$$

In the above equation  $T_h$  and  $T_c$  are the temperature of the hot and cold sides in degrees Kelvin, respectively, and  $\eta_C$  is the Carnot efficiency ( $\eta_C = (T_h - T_c)/T_h$ ) for power generation and  $\eta_C = (T_c)/(T_h - T_c)$  for refrigeration). In this case,  $ZT$  is taken at the average temperature between the two sides. As an example, for  $T_h$  and  $T_c$  of 400 and 300K, respectively, a  $ZT$  of 1, 2, and 3 corresponds to conversion efficiencies of

approximately 20%, 30%, and 35% of Carnot, respectively. Compression engines used for refrigeration typically operate at around 30% of Carnot efficiency, so thermoelectric modules with  $ZT > 2-3$  could potentially replace these with solid-state devices, which do not use compressed gases or any moving parts. Similarly, such devices could be used to salvage waste heat from automobile engines to either run hybrid electric engines or recharge batteries.

Currently, the most commonly used commercial thermoelectric material is Bi<sub>2</sub>Te<sub>3</sub> and its alloys, which have  $ZT \approx 1$  at room temperature. Over the past five decades, however, it has been challenging to increase  $ZT$  above 1, since the parameters of  $ZT$  are generally interdependent.<sup>216–218</sup> For a given material,  $\rho$  decreases with increasing charge carrier concentration but so does  $S$ , while  $k$  increases. Both electrons and lattice vibrations, called phonons, can conduct heat. As a result,  $k$  can be decomposed into two terms,  $k_l$  and  $k_e$ , which correspond to the lattice and electronic contributions to thermal conductivity, respectively. At very high carrier concentrations, such as in metals,  $k_e$  contributes significantly to the total  $k$  and reduces  $S$  significantly, thus diminishing  $ZT$ . At the other extreme, insulators generally have a low  $k$  and high  $S$  but  $\rho$  is too large for the material to exhibit a high  $ZT$ . As a general rule,  $ZT$  is maximized when the carrier concentration is about 10<sup>19</sup>–10<sup>21</sup> cm<sup>-3</sup>, corresponding to highly doped semiconductors and semimetals.<sup>216,217,219</sup>

Mingo and Dresselhaus and co-workers predicted that dimensionally confined materials may exhibit greatly enhanced thermoelectric performance due to changes in their electronic band structure.<sup>220–226</sup> As  $\rho$  depends on the electronic density of states (DOS) and  $S$  depends on the energy derivative of the DOS near the Fermi energy, sharp increases in the DOS due to electronic confinement may significantly improve these parameters. Experimental results have borne out the predicted enhancement of  $S$  or the power factor ( $S^2/\rho$ ) with electronic confinement in nanostructured materials, but none have demonstrated an overall improvement in  $ZT$ .<sup>227–229</sup> Nanowire materials have been investigated as promising materials in this respect for some time, though with mixed results. As the diameter or grain size of nanowires composed of Bi and its alloys (Se, Te, and Sb) decreases, surface scattering of electrons dominates and the transport measurements become sample dependent.<sup>230–233</sup> Several groups have observed enhanced Seebeck coefficients in disordered thin films of Bi alloy and PbTe nanowires and nanorods, though the characterization of these films is poor, and hence, the cause of this enhancement remains unclear.<sup>234–239</sup> The thermoelectric properties of individual InSb and CrSi<sub>2</sub> nanowires have also been measured and were found to suffer from similar surface effects.<sup>240–242</sup> In another approach, field-effect gating of thermoelectric nanowires to manipulate the DOS near the Fermi level has been predicted<sup>243</sup> and shown to effectively enhance  $S$  and  $ZT$  overall,<sup>244</sup> though not enough to improve upon state-of-the-art bulk materials. Recent experimental work on thallium-doped bulk PbTe demonstrated for the first time that increasing the density of states near the Fermi level does lead to a 2-fold increase in  $ZT$  (from 0.7 to 1.5 at 800 K), though this result is irrespective of electronic confinement.<sup>245</sup> Consequently, nanoscale manipulation of the electronic band structure as a means to improve the thermoelectric properties of materials is still a work in progress. The large theoretical enhancements in  $ZT$  should motivate future research in

surface passivation, through chemistry or heterostructures, to realize these predictions.

Nanostructured thermoelectric materials have shown great promise as several groups have observed  $ZT$  values of 1.5–2.5.<sup>246–248</sup> Electronic confinement and subsequent changes in the electronic band structure, however, are not the sources of this  $ZT$  enhancement. In these recent, high- $ZT$  bulk nanostructured materials, the electronic properties,  $\rho$  and  $S$ , remain similar to homogeneous bulk materials. Their  $k$ , on the other hand, is significantly lower. All other material parameters being equal, this decrease in thermal conductivity is the cause of  $ZT$  enhancement in nanostructured bulk materials.<sup>218</sup> The nanostructures incorporated either through controlled growth methods, such as molecular beam epitaxy (MBE),<sup>246,248</sup> or by precipitation during crystallization<sup>247,249</sup> evidently play a crucial role in reducing the thermal conductivity of these materials.

Nanostructures in these materials impede the flow of heat by increasing the rate of phonon scattering. Treating phonon transport as purely diffusive, the Boltzmann transport equation gives the thermal conductivity as  $k = 1/3Cv_l$ , which can also be expressed with frequency dependence by  $k = 1/3\int C(\omega)v(\omega)l(\omega) d\omega$ , where  $C(\omega)$ ,  $v(\omega)$ , and  $l(\omega)$  are the phonon frequency-dependent heat capacity, group velocity, and mean free path, respectively. Assuming that incorporated nanostructures do not change the phonon dispersion relation,  $C(\omega)$  and  $v(\omega)$  will essentially be the same as in the bulk homogeneous material.  $l(\omega)$ , on the other hand, is proportional to the relaxation time,  $\tau$ , of different phonon scattering mechanisms within the crystal lattice. For multiple simultaneous scattering processes, the scattering probabilities, related to the inverse of  $\tau$  corresponding to each process ( $\tau_1$ ,  $\tau_2$ , etc.), are additive, such that  $l_{\text{ph}}^{-1}(\omega) = l_1^{-1}(\omega) + l_2^{-1}(\omega) + \dots$ . This relation implies that  $l_{\text{ph}}$  of a given phonon mode (i.e., frequency) will be limited by the scattering mechanism with the highest probability, i.e. the shortest  $l$ .

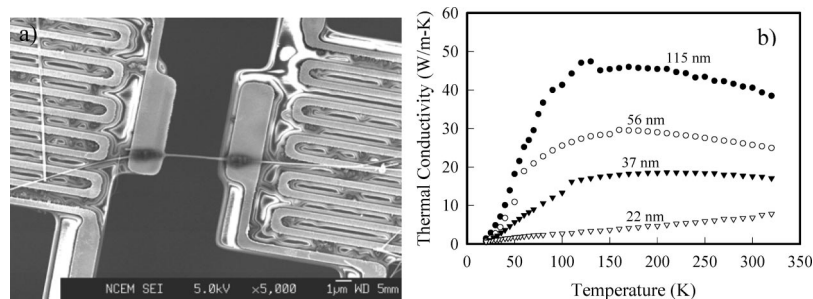
Keeping in mind the frequency dependence of the various relaxation times, phonon modes of different wavelengths will scatter with different probabilities by each process. For example, random atomic impurities in the lattice may scatter short wavelength phonons efficiently due to their comparable dimensions, whereas long-wavelength phonons spanning many unit cells may pass unaffected. Tailoring the dimensions of various scattering elements provides a promising approach for broadband impedance of phonon transport. Nanostructures, in particular, play a significant role in scattering phonons and can lead to thermal transport properties which significantly differ from bulk behavior.<sup>250</sup> Thus, by careful incorporation of atomic- and nanoscale scattering sites, novel materials may be fabricated with superior thermoelectric performance.

Point defects or impurities have long been known to impact the lattice thermal conductivity of materials.<sup>251–253</sup> Even isotopic impurities affect low-temperature phonon transport.<sup>254</sup> At very low temperatures, boundary and impurity scattering limit phonon transport. The peak thermal conductivity occurs at temperatures where high-energy modes begin to scatter by phonon–phonon interactions, called Umklapp processes. This peak typically occurs below 50 K, and temperatures above this will be referred to, generally, as “high temperatures”. Umklapp scattering scales as  $\omega^2$ , such that at high temperatures most of the high-frequency modes are scattered, leaving the long-wavelength modes to dominate phonon transport.<sup>255</sup> Generally, materials with low lattice

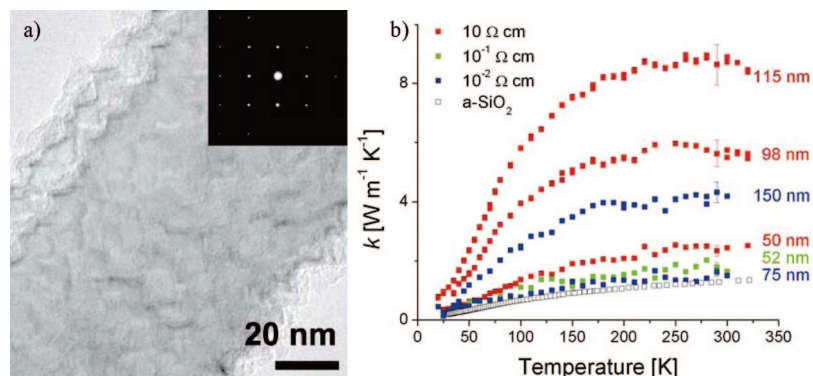
thermal conductivity values near room temperature are those with low symmetry, polyatomic, ionic lattices, and large mass disparities between their constituent atoms. Common examples include the lead chalcogenides, bismuth telluride and its alloys, perovskites, and spinel structures and their alloys. This mass asymmetry, along with bond anharmonicity, prevents the propagation of some of the long-wavelength modes, thus reducing the overall thermal conductivity near room temperature and above to several  $\text{W m}^{-1} \text{K}^{-1}$ . Highly symmetric, monatomic, covalent lattices of light elements, such as silicon and diamond, on the other hand, conduct heat very well, on the order of 100 and 1000  $\text{W m}^{-1} \text{K}^{-1}$ , respectively. Consequently, long-wavelength phonons have significantly longer mean free paths in these materials. It is precisely because of this high thermal conductivity that bulk silicon is a poor thermoelectric material ( $ZT = 0.01$ ).

Silicon has recently become an interesting material for thermoelectric research, however, due to the long mean free path of phonons that dominate heat transport in silicon at high temperature. Mid- to long-wavelength phonons are efficiently scattered at boundaries and nanoscale interfaces.<sup>252,256</sup> Goodson and co-workers examined the thermal conductivity of thin silicon films, down to less than 100 nm, and found significant reductions in thermal conductivity at high temperatures. In contrast to the work on layered nanostructures deposited by MBE, where layers were of the order of nanometers in thickness, Goodson and co-workers discovered that the thermal conductivity of silicon decreased in layers thinner than about a micrometer.<sup>257,258</sup> These findings suggest that some long-wavelength phonons propagate more than a micrometer before scattering in bulk silicon at room temperature. They estimated that the mean free path of phonons in silicon at room temperature is close to 300 nm, and films less than 100 nm thick have a thermal conductivity one-half that of bulk.<sup>259</sup> In contrast, electrons in highly doped silicon, i.e., with optimal electrical conductivity for thermoelectric applications, have a mean free path of 1–10 nm at room temperature.<sup>26,260</sup> Materials with characteristic length scales within the range of tens to hundreds of nanometers should effectively decrease thermal conductivity while maintaining bulk electronic properties. This strategy has been explicitly realized in nanocrystalline p- and n-type BiSbTe<sup>261–263</sup> and p- and n-type SiGe<sup>264,265</sup> alloys with enhanced  $ZT$  values 1.5–2 times those of the corresponding bulk materials. Similar results were obtained in LAST-type materials<sup>249</sup> and InGaAs<sup>266</sup> with the intentional inclusion of nanoscale particles into crystalline lattices. Specifically, Kim and co-workers showed that the thermal conductivity nanostructured materials could be reduced beyond the alloy limit, i.e., the lowest  $k$  achievable by point defect scattering.<sup>266</sup> Semiconductor nanowires, with their extensive surface area and established electrical properties, are seemingly ideal structures to exploit this length scale disparity for improved thermoelectric performance.

Indeed, silicon nanowires exhibit interesting thermal transport behavior. Majumdar and co-workers developed a method by which to directly measure the thermal conductivity of individual nanowires using microfabricated devices.<sup>11,12,267</sup> Two thermally isolated resistive thermometers are bridged by a single nanowire which, to a very high accuracy, is the only channel for heat conduction between them. As the temperature on one side increases, the rise in temperature on the other side is measured, and the difference between the two is used to extract the thermal conductivity



**Figure 13.** (a) SEM image of the microfabricated device used to measure the thermal conductivity of individual nanowires. (b) Thermal conductivities of single-crystalline pure Si nanowires vary with diameter. The number besides each curve denotes the corresponding wire diameter. (Reprinted with permission from ref 11. Copyright 2003 AIP.)



**Figure 14.** Rough silicon nanowires. (a) TEM image of an EE silicon nanowire and selected area electron diffraction pattern indicating the nanowire is single crystalline (inset). (b) Temperature-dependent  $k$  of EE Si nanowires etched from wafers of different resistivities:  $10 \Omega \text{ cm}$  (red squares),  $10^{-1} \Omega \text{ cm}$  (green squares; arrays doped postsynthesis to  $10^{-3} \Omega \text{ cm}$ ), and  $10^{-2} \Omega \text{ cm}$  (blue squares). For the purpose of comparison, the  $k$  of bulk amorphous silica is plotted with open squares. The smaller highly doped EE Si nanowires have a  $k$  approaching that of insulating glass, suggesting an extremely short phonon mean free path. Error bars are shown near room temperature and should decrease with temperature. (Reprinted with permission from ref 272. Copyright 2008 Nature.)

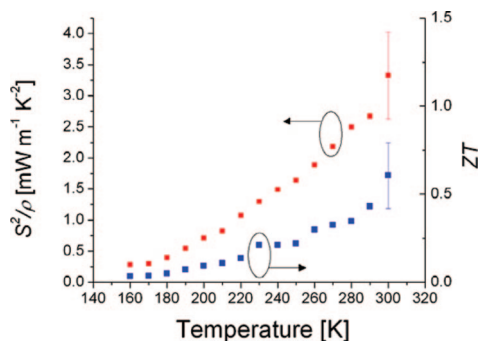
of the nanowire. Li and co-workers found that the  $k$  values of silicon nanowires synthesized by the VLS mechanism were diameter dependent, in agreement with the thickness dependence in the thin film studies, but up to 10- to 20-fold lower than bulk at room temperature (Figure 13).<sup>11</sup> These results can be explained by assuming diffusive phonon scattering at the surfaces, such that the mean free path is limited by the diameter of the nanowire,  $l(\omega) \approx d$ .<sup>268</sup> Interestingly, the narrowest nanowire measured, 22 nm in diameter, exhibited an anomalous linear dependence of  $k(T)$ , whereas  $k$  typically varies as  $\sim T^3$  in bulk at low temperatures. Further studies on small-diameter nanowires ( $<30 \text{ nm}$ ) found the same consistent linear dependence up to about 100 K.<sup>269</sup> Theoretical modeling explains this behavior as a pseudolinear temperature dependence as a result of overlap between ballistic and diffusive transport regimes. This theory suggests that phonon scattering at the nanowire surface is actually a frequency-dependent process, such that not all  $l(\omega) = d$ .

Incorporating nanoscale roughness on the surfaces of silicon nanowires serves to further reduce the transmission of phonons along their length. Arrays of rough nanowires have been synthesized by an electroless wafer etching technique (EE) in an aqueous solution of silver nitrate and hydrofluoric acid. The reaction proceeds by  $\text{Ag}^+$  reduction to silver metal on the wafer surface by oxidation of the surrounding silicon lattice, which is then dissolved by HF in solution. The silver metal forms a mesh-like network which etches its way vertically into the wafer, leaving behind high aspect ratio silicon pillars.<sup>39,270,271</sup> The arrays etch vertically into the silicon, up to tens of micrometers, irrespective of the wafer crystallographic orientation, dopant concentration, or type. TEM analysis of the EE nanowires

reveals that they are single crystalline with rough surfaces but otherwise defect free (Figure 14a).<sup>272</sup>

Thermal transport measurements of these nanowires show a diameter-dependent thermal conductivity much like that of the VLS-grown nanowires but a factor of 5–10 lower at corresponding diameters (Figure 14b). The difference is greater for smaller diameter nanowires. Furthermore, the peak thermal conductivity occurs near room temperature for the EE nanowires, as compared to 150–200 K for the VLS nanowires and 25 K for bulk silicon.<sup>272</sup> The late onset of Umklapp scattering in the rough nanowires suggests that the nanowire surfaces scatter phonon efficiently up to high temperatures. It is possible that the roughness behaves like nanoscale particles at the surface, disrupting the lattice periodicity and leading to higher rates of diffuse or back-scattered phonon reflections. These mechanisms have been theorized to reduce thermal conductivity in silicon nanowires<sup>273,274</sup> but not the extent observed for the EE nanowires. Thermal transport measurements of  $\text{PbX}$  ( $\text{X} = \text{S}, \text{Se}, \text{Te}$ ) nanowires also show that the peak in thermal conductance as a function of temperature decreases in magnitude and shifts to higher temperatures, or disappears altogether, with decreasing nanowire diameter.<sup>275</sup>

The thermal conductivity of EE nanowires further decreases with increasing surface roughness. To optimize the electronic properties of the nanowires for thermoelectric applications, the arrays were doped to  $\sim 10^{19} \text{ cm}^{-3}$  by annealing in  $\text{BCl}_3$  vapor. The thermal conductivities of the smallest nanowires, 50–75 nm in diameter, approach  $1.6 \text{ W m}^{-1} \text{ K}^{-1}$  at room temperature, a full 100-fold decrease from bulk values.<sup>276</sup> By subtracting the electronic contribution using the Wiedemann–Franz law, the lattice contribution



**Figure 15.** Single silicon nanowire power factor (red squares) of the nanowire and calculated  $ZT$  (blue squares). Reprinted with permission from ref 272. Copyright 2008 Nature.)

to thermal conductivity was calculated as  $\sim 1.2 \text{ W m}^{-1} \text{ K}^{-1}$ ,<sup>260,272</sup> which is surprisingly close to the amorphous limit for silicon and the thermal conductivity of silica at room temperature.<sup>277,278</sup>

That a roughly 50 nm single-crystalline nanowire conducts heat like an amorphous insulator is an unexpected result. Assuming diffusive reflections of phonons from the nanowire surface, the mean free path is limited to the nanowire diameter, and a model based on Boltzmann transport theory can explain the thermal conductivity trends in VLS-grown nanowires but grossly overestimates the values for EE nanowires.<sup>268</sup> On the other hand, the lattice thermal conductivity of amorphous solids can be estimated assuming the mean free path is limited to one-half the phonon wavelength due to the lack of extended coherence over a disordered lattice.<sup>278</sup> The nanowire channel, however, is single crystalline, and hence, there is no reason to believe phonon modes would lose coherence over a distance  $l = \lambda/2$ , especially considering that the diameter is about 10 times the lattice constant. Conventional models fail to describe the observed phonon transport behavior, but the comparison between VLS and EE nanowires suggests the roughness plays a significant role.

The electronic properties of individual nanowires were measured in order to assess the  $ZT$  of EE nanowires. Conventional photolithographic processing was used to pattern metal electrodes onto the dispersed nanowires. The temperature-dependent resistivity and Seebeck coefficient were both measured for each nanowire (Figure 15). For a  $\sim 50$  nm EE nanowire at room temperature, the power factor,  $S^2/\rho$ , was nearly that of bulk silicon at the same dopant concentration. Since the thermal conductivity was almost 100-fold lower,  $ZT$  improved nearly 100-fold to 0.6. Though thermal data could not be gathered for thinner nanowires, electrical characterization of thinner EE nanowires suggests  $ZT$  should improve further near room temperature and surpass 1 at elevated temperatures.<sup>272</sup> Boukai and co-workers found similar results for lithographically defined silicon nanowires, though their  $ZT$  peaked at around 200 K.<sup>279</sup> The diameter of these nanowires was significantly smaller than in the previous study, 10–20 nm, and measurements of their electronic properties suggest that phonon confinement effects may enhance the Seebeck coefficient at this size scale. The measured  $ZT$  values approaching that of commercial  $\text{Bi}_2\text{Te}_3$  demonstrate that silicon nanowires are a viable and promising thermoelectric material. Silicon is also the most abundant semiconductor and has the added advantage of a large industrial infrastructure suited to large-scale, inexpensive processing. Moreover, the robust aqueous synthesis, wafer-

scale processing, and vertical alignment of the EE nanowire arrays makes them ideal for thermoelectric module fabrication and large-scale manufacturing.

## 7. Concluding Remarks

Semiconductor nanowires, as a new class of nanomaterials, are currently the subject of extensive research with several thousands of papers published annually in the field. Nanowires are model systems for investigating the dependence of optical, electrical, magnetic, and mechanical properties on dimensional confinement. Going forward, they are promising as both interconnects and functional components in the fabrication of nanoscale optoelectronic and energy conversion devices.

Harvesting energy from the ambient environment provides a potentially endless source of energy, whether for increasing the efficiency of existing generation processes, charging batteries quasi-continuously, or enabling self-powered devices. Various types of ambient power can be harnessed, including solar, thermal, vibrational, fluidic, and electromagnetic energy. Nanowires, with their unique capability of bridging nanoscopic and macroscopic worlds, promise novel and efficient strategies for tapping into these energy sources, as discussed in this review. One emerging and exciting direction is their application to solar to fuel conversion. The generation of fuels by the direct conversion of light energy to chemical bonds in a single device is an attractive goal, but no such system has been demonstrated that shows the required efficiency, is sufficiently durable, or can be manufactured at a reasonable cost. This is one of the most challenging and complex problems facing chemists and physicists alike.

The direct solar-to-fuel approach is inspired by nature's photosynthetic organisms, which accomplish the conversion of carbon dioxide and water to carbohydrates in a single integrated system. The carbon-fixing scheme in nature displays crucial design features for an engineered solar-to-fuel conversion system: spatial and directional arrangement of the light-harvesting components, charge separation and transport, and conversion of the stored potential to chemicals at catalytic sites in compartmentalized spaces. To engineer efficient and durable systems for solar-to-fuel conversion, the rational design, synthesis, and assembly of the inorganic/organic/hybrid nanostructures at multiple length scales must take advantage of these concepts and structures. Semiconductor nanowires can be readily designed and synthesized to deterministically incorporate heterojunctions that promote charge separation and directional transport as well as to selectively position the different catalysts. It is, therefore, not surprising that these unique one-dimensional structures will be part of the solution to this challenging problem. The rapid pace of research in the field of one-dimensional nanostructures has been driven by the exciting scientific challenges and technological potential of these nanoscopic systems. The success of nanowire research has been, and will be, largely built on the synthetic control scientists can achieve for this class of nanostructures.

## 8. Acknowledgments

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# Semiconductor Nanowire: What's Next?

Peidong Yang,\* Ruoxue Yan, and Melissa Fardy

Department of Chemistry, University of California, Materials Sciences Division, Lawrence Berkeley National Laboratory, Berkeley, California 94720

**ABSTRACT** In this perspective, we take a critical look at the research progress within the nanowire community for the past decade. We discuss issues on the discovery of fundamentally new phenomena versus performance benchmarking for many of the nanowire applications. We also notice that both the bottom-up and top-down approaches have played important roles in advancing our fundamental understanding of this new class of nanostructures. Finally we attempt to look into the future and offer our personal opinions on what the future trends will be in nanowire research.

**KEYWORDS** Nanowire, photonics, energy conversion, bio-nano interface

When R. S. Wagner wrote down the following equation  $R_{\min} = [(2V_1)/(RT \ln s)]\sigma_{lv}$  for vapor–liquid–solid whisker growth, where  $R_{\min}$  is the minimum whisker radius,  $V_1$  is the molar volume of the metal droplet,  $\sigma_{lv}$  is the liquid–vapor surface energy, and  $s$  is the degree of supersaturation of the vapor,<sup>1</sup> he probably did not expect that half a century later research into semiconductor whiskers with nanoscale dimensions would become such an active field. In the early 1990s, Hitachi scientists picked up this growth technique and applied it to the growth of III–V nanowhiskers.<sup>2,3</sup> At the time, good positional and orientational control was achieved as well as the demonstration of the first pn junctions based on heterostructured nanowhiskers.<sup>4</sup>

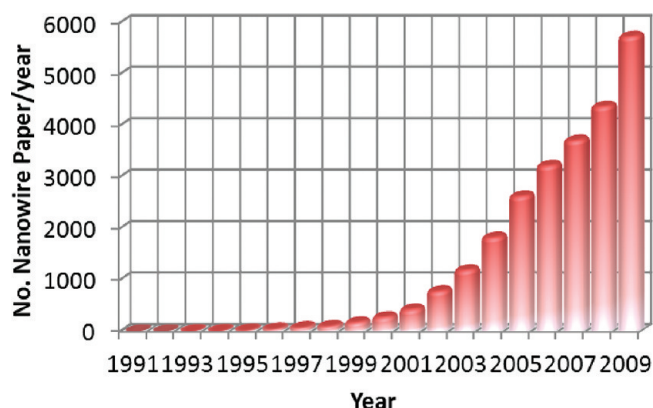
**The field of semiconductor nanowire has become one of the most active research areas within the nanoscience community.**

This was followed by important studies in two separate research laboratories in the mid 1990s. A new whisker growth mechanism was proposed by William Buhro's group at Washington University, where they synthesized III–V nanowhiskers using a solution–liquid–solid process.<sup>5</sup> Meanwhile, the Charles Lieber group at Harvard initiated a research program in the area of inorganic nanorods. Carbide<sup>6</sup> and oxide nanorods<sup>7</sup> were produced

through vapor phase conversion and transport processes in several of these early studies.

These early works on nanowhiskers and nanorods were popularized as semiconductor nanowires in the following decade. In the late 1990s, the field of semiconductor nanowires underwent a significant expansion and became one of the most active research areas within the nanoscience community.<sup>8</sup> Exciting developments have been made at an unusually fast pace from many research groups worldwide, including notably the Lieber group, the Yang group, the Samuelson group at Lund University, and the Wang group at Georgia Tech. Initially the field went through a quite hyped period and has now developed into a relatively mature research area with ample exciting and new research opportunities. This can be seen from the vast number of papers (Figure 1) published on nanowires over the past two decades, which has increased exponentially, with most of the activity and development happening in the last ten years.

Just like any other emerging research frontier, people working in the nanowire field are constantly pushing the envelope to develop new fundamental science as well as



**FIGURE 1.** Increase in the number of publications on nanowire-related topics from year 1991–2009 (Source, ISI; keyword, nanowires).

\* To whom correspondence should be addressed. E-mail: p\_yang@berkeley.edu.  
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Authorship	Paper title Journal, Issue, Page no., Year of publication	No. of times cited
Huang, M.H., et al.	Room-temperature ultraviolet nanowire nanolasers <i>Science</i> , 292, 1897 (2001)	3,567
Xia, Y.N., et al.	One-dimensional nanostructures: Synthesis, characterization, and applications <i>Advanced Materials</i> , 15, 353 (2003)	2,989
Pan, Z.W., et al.	Nanobelts of semiconducting oxides <i>Science</i> , 291, 1947 (2001)	2,886
Morales, A.M., et al.	A laser ablation method for the synthesis of crystalline semiconductor nanowires <i>Science</i> , 279, 208 (1998)	2,287
Cui, Y., et al.	Nanowire nanosensors for highly sensitive and selective detection of biological and chemical species <i>Science</i> , 293, 1289 (2001)	1,793
Hu, J.T., et al.	Chemistry and physics in one dimension: Synthesis and properties of nanowires and nanotubes <i>Accounts of Chemical Research</i> , 32, 434 (1999)	1,765
Duan, X.F., et al.	Indium phosphide nanowires as building blocks for nanoscale electronic and optoelectronic devices <i>Nature</i> , 409, 66 (2001)	1,531
Cui, Y., et al.	Functional nanoscale electronic devices assembled using silicon nanowire building blocks <i>Science</i> , 291, 851 (2001)	1,324
Huang, M.H., et al.	Catalytic growth of zinc oxide nanowires by vapor transport <i>Advanced Materials</i> , 13, 113 (2001)	1,261
Gudiksen, M.S., et al.	Growth of nanowire superlattice structures for nanoscale photonics and electronics <i>Nature</i> , 415, 617 (2002)	1,041

FIGURE 2. Top 10 most cited papers in the field of nanowire research (Source: ISI, Feb 2010).

potential applications. Several important subfields have emerged and each represents an exciting direction for both discovery-based and hypothesis-driven research. These subfields include nanowire electronics, nanowire photonics, nanowires for energy conversion and storage, and interfacing nanowires with living cells. One can get a quick idea of some of these exciting research topics by taking a glimpse at the top 10 most cited papers in this field (Figure 2), where some of the original ideas and concepts were first proposed or reported and were followed with overwhelming research activities worldwide.

It is not our intention to do a comprehensive research survey within this short perspective article. There are now many excellent review articles on this topic where readers can get a complete picture of nanowire research along various directions.<sup>8–11</sup> Instead we would like to provide some of our personal views on nanowire research by examining the latest research trends and activities. Some of the writing is meant to be forward-looking, speculative, or even controversial, but hopefully it can stimulate more creative thinking and even more innovative ideas about nanowire research itself.

**What is Fundamentally Different?** This is a question we often ask ourselves when looking to embark on a new research direction or invest our efforts in a new type of material. Similarly, when starting to work with semiconductor nanowires, we would like to know to what extent are they different from nanocrystals and nanotubes. Will this research lead to new science or discovery of new phenomena? Will it lead to new applications? The answer is clearly yes based on the past decade's research.

The unique one-dimensionality inherent to nanowires has already solved some of the long-standing technical problems that have plagued the thin film community. For example, integration of optically active semiconductors (e.g., III–V's) onto silicon is critical for the next generation of computing tools that will merge photonics with electronics on a single platform. The conventional thin film technologies have run into interfacial lattice mismatch issues that often result in highly defective optical materials. In this regard, nanowire growth provides a natural mechanism for relaxing the lattice strain at the interface and enables dislocation-free semiconductor growth on lattice mismatched substrates, for example, GaAs on silicon and germanium.<sup>12,13</sup>

The traditional semiconductor industry is unquestionably very advanced. For many decades, it has produced the devices and systems that are part of our daily lives, including transistors, sensors, lasers, light-emitting diodes, and solar panels. By changing the material morphology from bulk to nanowire form, one might wonder how much fundamental difference there is. There have been a few good surprises, many of which are the result of strong photon, phonon, and electron confinement within the semiconductor nanowires.

About 10 years ago, we first introduced the idea of semiconductor nanowire nanolasers.<sup>14</sup> Using ZnO nanowires as a model system, we were able to clearly demonstrate that lasing is possible for nanostructures with sub-wavelength cross sections.<sup>15</sup> Later the idea was applied to various semiconductors with different emission wavelengths covering from UV all the way to IR.<sup>16</sup> Developing a nanoscopic, coherent light source with an extremely small footprint has many important implications. It can be used in integrated photonic circuits, in miniaturized sensor platforms with low-power consumption, and as imaging probes with high spatial resolution. These lasing studies on one-dimensional nanoscale cavities have led to recent efforts on plasmonic assisted nanowire<sup>17</sup> and nanopillar<sup>18</sup> lasing where optical modes can be further compressed at the metal-semiconductor interface, and nanoscopic lasers with all three dimensions less than one wavelength are well on the horizon.

Another good example of a fundamental difference in nanowires is the recent demonstration of silicon nanowire thermoelectrics. Using vapor–liquid–solid grown silicon nanowires, it was established early in this lab that their thermal conductivity can be significantly reduced from the bulk value of 150 (at 300 K) to ~8 W/m K.<sup>19</sup> This size-dependent reduction in thermal conductivity is a direct result of strong phonon boundary scattering at the nanowire surface. Only recently it has been demonstrated that the thermal conductivity in silicon nanowires can be further reduced down to almost the amorphous limit through a surface defect engineering process.<sup>20</sup> As a result, these rough silicon nanowires behave totally differently from

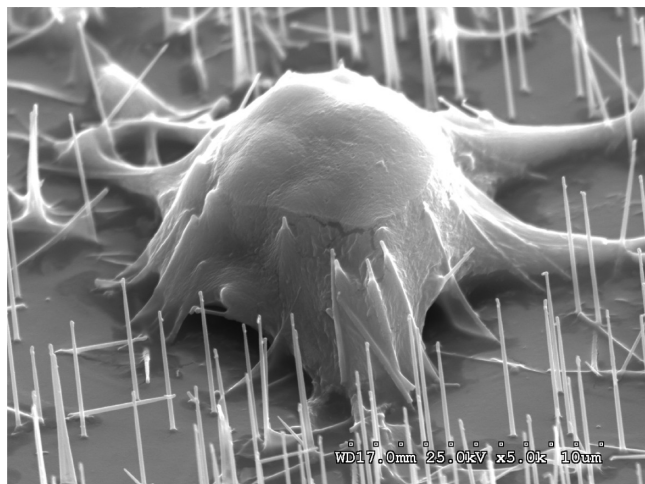


FIGURE 3. SEM image of a mouse embryonic stem (mES) cell interfaced with silicon nanowires.<sup>24</sup>

their bulk counterparts. At room temperature, bulk silicon is considered to be both a good thermal conductor and electron conductor, while rough silicon nanowires are essentially thermal insulators and at the same time good electron conductors, making them good thermoelectric materials for waste heat recovery and power generation at a relevant temperature range.

Another emerging area of great interest is the interfacing of semiconductor nanowires with living cells. After years of nanowire research, we now have a good understanding of their electrical and optical functionalities. They can be used as transistors<sup>21</sup> and subwavelength optical waveguides.<sup>22,23</sup> Their cross sections are much smaller than the typical cell dimensions, which means minimal invasiveness, while their high surface area ensures proper cell–nanowire communication (Figure 3). All of these considerations make the nanowire a powerful platform to interface with living cells for various purposes.<sup>24–26</sup> This includes interfacing with stem cells for precise delivery of small molecules, DNA, and proteins, and their potential use as a universal platform for guided stem cell differentiation. In another tour de force demonstration, the Lieber group has successfully integrated high density silicon nanowire transistor arrays for the detection, stimulation, and inhibition of neuronal signal propagation.<sup>27</sup> In addition, the multiplexed signal recording capability of entire nanowire transistor arrays has enabled temporal shifts and signal propagation to be determined with very good spatial and temporal resolution. This research direction of interfacing nanowires and living cells is certainly one of the most exciting topics at the moment and is quickly unfolding. While it is true that the nanowire in this case is serving as a versatile technological tool or platform, many new discoveries are expected when such platforms are used to tackle real biological problems.

**The Issue of Performance Benchmarking.** If we dive into the vast amount of nanowire literature, or even the

more broadly defined nanoscience literature, we find that many of the papers claim novelty by demonstrating certain new nanostructure syntheses and/or their applications. These applications are often related to the structure's optical, electrical, and magnetic properties, where one can find related reports and discussion for their bulk counterparts in a separate research community. The infrequent comparison between nanostructured and bulk materials partly reflects the undisputed reality that there is indeed some disconnection between those who are working on semiconductor nanostructures and those who are working on more traditional bulk semiconductors. This also points out the fact that in the nanowire community we are often addressing the “yes-or-no” question due to novelty claim and priority issues, and we tend to pay less attention to the “better-or-worse” question, which is typically considered as an incremental, less-challenging, engineering optimization issue. In reality, both questions are equally important if we want to have the biggest impact from nanowire technology.

The high-efficiency and long-durability of semiconductor light-emitting diodes (LED) based on thin-film technology renders them ideal for displays and solid-state lighting.<sup>28</sup> However, further miniaturization of electrically driven multicolor light sources is crucial to a variety of fields including integrated nanophotonic and optoelectronic systems, high-resolution microdisplays, ultrahigh density optical information storage, and multiplexed chemical/biological analysis. This is where nanoscale LEDs come into play and semiconductor nanowires (III–V and nitrides) have been widely considered as good candidates for such applications. Since the first demonstration of a nanowire LED using vertical GaAs pn-junction arrays by the Hitachi team,<sup>4</sup> nanowire-based LEDs have been successfully realized in a variety of cross-nanowire junctions,<sup>29–31</sup> longitudinal<sup>32</sup> and coaxial heterostructures<sup>33</sup> with electroluminescence (EL) emission spanning the entire visible spectrum, as well as the near-infrared and ultraviolet. In particular, multicolor LEDs have been fabricated on a single substrate by contacting different n-type nanowires, including GaN (UV), CdS (green), and CdSe (near-infrared) with Si nanowires as a common p-type material,<sup>30</sup> enabling easy interfacing with conventional silicon microelectronics. However, in most of these demonstrations, quantum efficiency of the light-emitting devices was not reported, making it difficult to quantitatively compare with the existing thin film technologies. The highest external quantum efficiency (EQE) reported for nanowire-LEDs is in core/multishell nanowires that consist of n-GaN core and  $\text{In}_x\text{Ga}_{1-x}\text{N}/\text{GaN}/\text{p-AlGaIn}/\text{p-GaN}$  shells. The estimated EQE for these nanowire LEDs was 5.8% at 440 nm and 3.9% at 540 nm.<sup>33</sup> This result is already comparable to InGaIn-based single quantum-well thin film LEDs in the blue and green wavelength range.<sup>34</sup> And obviously there is still plenty of room for improve-

ment by optimizing collection efficiency and interface control to minimize undesired nonradiative recombination.

A similar comparison can be made between nanowire and conventional thin-film solar cells. Solar cells are being pursued as environmentally friendly, renewable power sources to ease the energy crisis currently confronting human society. However, it remains a challenge to make inexpensive, high-efficiency photovoltaic devices for large scale energy conversion. Semiconductor nanowires have been proposed to be fundamentally advantageous for photovoltaic applications due to their unique properties. Some of the many advantages that nanowires offer include long absorption path lengths while maintaining short distances for carrier collection/transport; interpenetrating heterojunction interfaces, allowing efficient carrier extraction following light absorption; strong light trapping in high-density nanowire arrays; and modification of material properties and cell efficiencies through size and composition variation of the nanostructures. Dye-sensitized solar cells using dense arrays of oriented, crystalline ZnO nanowires as the anode have been designed to improve charge collection efficiency.<sup>35</sup> The direct electrical pathways provided by the nanowires ensure the rapid collection of carriers generated throughout the device, and an efficiency of 2.5%<sup>36</sup> under AM 1.5 illumination has been demonstrated, limited primarily by the need to improve dye-loading by increasing the surface area of the nanowire array. Coaxial silicon nanowires (p-type/intrinsic/n-type (p-i-n)) have achieved an apparent energy conversion efficiency of up to 3.4% under AM 1.5 illumination and are able to drive functional nanoelectronic sensors and logic devices.<sup>37</sup> And more recently, strong light-trapping effects were observed in high-density silicon core-shell nanowire array solar cells with efficiencies up to 6%.<sup>38</sup> Integrated with ultralow power nanosystems, such as remote and mobile chemical and environmental sensors, nanoelectronic and nanophotonic circuitry, these nanowire-based photovoltaic devices may serve as clean and sustainable miniature power sources. However, it remains a scientific challenge to design and synthesize nanowires and their heterostructures with performances exceeding that of the existing silicon photovoltaic technology, which has a commercial efficiency larger than 20% and is being pushed to its theoretical limiting efficiency of 29%.<sup>39</sup> Issues to be considered include surface recombination problems associated with the high surface area of the nanowires.

A couple of years ago, a nanogenerator based on ZnO nanowires<sup>40</sup> was developed to power battery-free wireless nanodevices by converting ambient vibrations, hydraulic energy, or mechanical movement to electricity. These devices with their varying configurations<sup>41–44</sup> demonstrate the capability of harvesting energy from ambient vibrations in different media,<sup>41,45</sup> even from body and

muscle movements,<sup>46</sup> and under a variety of frequencies (<10 Hz to 41 kHz).<sup>41,42</sup> However, the major bottleneck to the application of these nanogenerators is their low output power, which is usually on the order of nanowatts (areal power density <1  $\mu\text{W}/\text{cm}^2$ )<sup>43,47</sup> and low output voltage, which is dictated by individual nanowires and is typically <100 mV.<sup>44,46,48,49</sup> In comparison, a commercial piezo generator based on piezoceramic thin films operating in the same frequency range can be 3–4 orders of magnitude higher in these parameters (e.g., piezoelectric heel-strike generators<sup>50</sup>). Although different strategies to improve the output power and voltage have been proposed,<sup>51</sup> these nanogenerators may never be able to power our homes or even our flash lights. However, ZnO nanowire-based nanogenerators possess unique features that make them promising solutions for independent, sustainable, maintenance-free nanosystems that have low-power consumption, such as implantable biosensors, nanorobotics, and possibly portable/wearable personal electronics.

While the previous section focused on the scientific breakthroughs achieved by constantly pushing the fundamental limits of nanowires, here we would like to make the case that a technological breakthrough will come only after we address both “yes-or-no” and “better-or-worse” issues with all of our undivided attention. Long-term stability will also be a common problem for many of the nanowire-based devices and systems. It is true for the LEDs, solar cells, and piezoelectric generators we discussed here, as well as sensors, batteries, and transistors. We need to keep in mind that all of these areas have existing mature industrial technologies (largely technologies based on thin film and bulk semiconductors). Unless there are significant cost and/or performance benefits from using nanowires, it will be difficult to displace the existing technologies.

**Bottom-up versus Top-down.** There are now many different methods for making semiconductor nanowires. They are commonly placed into two categories, namely, the bottom-up and top-down approaches. The bottom-up approach starts with individual atoms and molecules and builds up the desired nanostructures. For nanowire synthesis, this includes vapor-liquid-solid (VLS) chemical vapor deposition, the solid-liquid-solid process, nanopore templating, and various hydrothermal methods. The top-down approach relies on dimensional reduction through selective etching and various nanoimprinting techniques. Similarly, for the integration of nanowire building blocks, there are also two such general approaches, the first being chemical assembly such as Langmuir-Blodgett techniques, the other being lithographical processes. So, which one is better?

The answer as of now is a bit of both. It is clear at this point that we need both approaches for many of the applications related to semiconductor nanowires. Synthesis-



based bottom-up approaches can be used to generate structures with dimensions ranging from angstroms to hundreds of nanometers. On the other hand, while the fabrication based top-down processes still have difficulty creating structures below 10 nm, they have a great advantage when integration and addressability are concerned.

There are a couple of good examples that illustrate the value of both strategies. The thermoelectric performance of silicon nanowires has been examined in both synthetic and fabricated nanowires. The Yang group has focused on VLS grown and chemically etched nanowires<sup>20</sup> while the Heath group has had great success with their Superlattice Nanowire Pattern Transfer (SNAP) process to produce very thin silicon nanowires.<sup>52</sup> In both cases, similar thermoelectric performance was observed.

The concept of nanowire based solar cells has attracted significant attention because of their potential benefits in carrier transport, charge separation, and light absorption. The Lieber group has developed a fairly sophisticated core–sheath growth and contact strategy for their silicon p-i-n nanowire solar cells with efficiencies up to 3.5%.<sup>57</sup> On the other hand, etching and overgrowth processes have also been developed to fabricate high density silicon core–shell solar cells. Efficiencies up to 6% have been demonstrated in this type of top-down fabricated nanowire core–shell arrays.<sup>58</sup> Here, both synthetic and fabricated nanowires offer similar core–shell solar cell designs, and therefore the same operational principle, but with differences in interface sharpness and carrier recombination kinetics.

We can find a similar situation when it comes to sensor and laser research. On the basis of chemically synthesized nanowires, chemical/biological sensors<sup>53–55</sup> and nanolasers<sup>14,56,57</sup> have been developed in the Lieber and Yang groups. On the other hand, fully integrated nanowire chemical sensors<sup>58</sup> and nanopillar lasers<sup>18</sup> have been fabricated and tested using all top-down processes. The top-down approach typically has the advantage of being high throughput and more amenable for large scale integration.

As for the integration of ultrahigh density nanowire circuitry, this has been one of the major challenges for nanowires prepared by either method. We have many different methods to assemble high-density nanowire arrays in either two or three dimensions. However, when one wants to carry out large scale integration, the choices are fairly limited, especially for addressability at the single nanowire level. In this regard, the SNAP method proposed by the Heath group is quite promising.<sup>59</sup> They have proposed a demultiplexer architecture for bridging the submicrometer dimensions of lithographic patterning to the nanometer-scale dimensions for ultrahigh-density nanowire circuits. This concept has been experimentally demonstrated on submicrometer wires and on an array of 150

silicon nanowires patterned at nanowire widths of 13 nm and a pitch of 34 nm.<sup>60</sup>

**Where Is the Killer Application?** So where is the killer application after all these years? Or is there one?

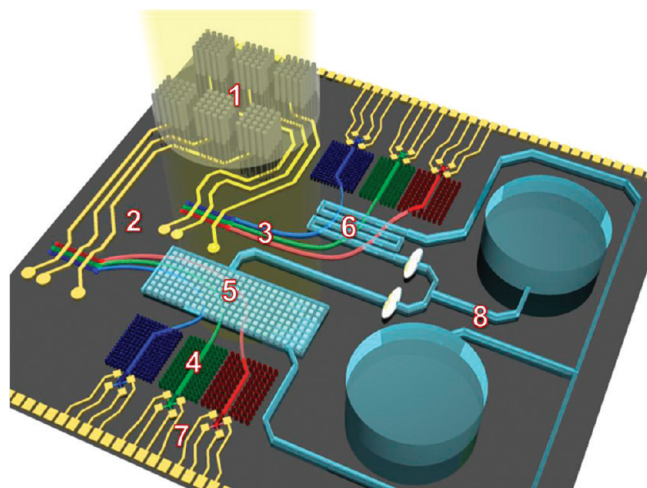
Optimistically, the field has made great progress in advancing the fundamental science related to semiconductor nanowires. There are also many startups trying to commercialize some of the important applications, including Nanosys, Alphabet Energy, Vista Therapeutics, and QuNano. Below are three areas, in our opinion, that will see great research activity and progress in the coming years.

**Integrated Nanophotonics.** Imagine what you can do if you can have a nanoscopic light source that is as small as a semiconductor transistor.

The ability to manipulate pulses of light within submicrometer volumes is vital for highly integrated light-based devices, such as optical computers, to be realized. Chemically synthesized nanowires have several unique features that make them good photonic building blocks, including inherent one-dimensionality, a variety of optical and electrical properties, good size control, low surface roughness and, in principle, the ability to operate both above and below the diffraction limit. While state-of-art lithography techniques are capable of fabricating nanostructured features with dimensions discussed in this article, chemically grown nanowires still possess unique advantages such as single crystallinity, atomically smooth surfaces and low defect density. Using a combination of nanolithographic tools, it is highly feasible to assemble photonic circuits (Figure 4) from a collection of nanowire elements that assume various functions, such as light creation, routing, and detection. Since the range of nanowire materials now includes active, passive, nonlinear and semiconducting inorganic crystals,<sup>9</sup> synthesizing from the bottom-up offers novel design-by-choice schemes to facilitate the assembly of multifunction components on the same substrate. Such systems can be used in parallel with existing sensing technologies such as surface-enhanced Raman spectroscopy and microfluidic systems, and could be used in many potential areas such as chemical/biological sensing, environmental monitoring, imaging, and information processing. Because of their small footprint and low power consumption, they can be self-powered and readily embedded in cell phones or wrist-watches.

**Nanowire-Based Single Cell Endoscopy.** The development of a nanoscopic coherent light source could also lead to a new type of nanowire probe for *in situ* single cell imaging, essentially a nanowire-based single cell endoscopy.

Development of such nanowire probes would enable us to carry out intracellular imaging and probing with high spatial resolution (Figure 5), monitor *in vivo* biological processes within single living cells and greatly improve



**FIGURE 4.** Future nanophotonic circuits for chemical analysis will require the integration of nanowire-based devices that assume different functions, such as nanowire solar cells as on-chip power supplies (1), laser diodes/LEDs as light sources (2), nanoribbons (3) and 2D photonic bandgap (PBG) nanowire arrays (4) as filters and waveguides to select and route input and output signals; (6) sample analysis chambers, such as silver nanocube arrays for SERS (5) or nanoribbon/sample intersection for absorption analysis; nanowire based photodetectors (7) and a microfluidic system for liquid sample transport (8).

our fundamental understanding of cell functions, intracellular physiological processes, and cellular signal pathways. Nanowires have several key features that make them promising for applications in cell endoscopy including minimal invasiveness, high flexibility, high refractive index, evanescent wave optical sensing, and nonlinear optical conversion capabilities. The nanowires used for these probes will generally have diameters below 100 nm and high-aspect ratios, ensuring the noninvasiveness of the nanowire probes. The nanowires are also highly flexible and yet mechanically robust. The twisting and bending in these nanowires will not cause significant optical propagation losses,<sup>22</sup> and will greatly facilitate the application of such probes in single cell imaging. Because of their high refractive index, nanowires also function as efficient subwavelength optical waveguides, even in high-index physiological liquids and/or living cell environments.<sup>61</sup> Waveguiding also enables highly localized excitation and detection, limiting the probe volume to the very tip of the nanowire (i.e., down to pico- and femtoliter). Additionally, the application of nonlinear optical nanowires into the probe platforms will introduce two important features,<sup>62</sup> subwavelength waveguiding and frequency conversion capabilities. This will enable the input of an IR beam at one end of the nanowire and the use of visible or UV output on the other end for the cell imaging/probing. The use of IR as an input beam would greatly benefit the entire imaging process in realistic physiological environments.

Such novel nanowire probes promise intracellular imaging with greatly enhanced three-dimensional spatial

resolution as well as temporal resolution. In addition, these nanowire probes could be used for spot-delivery or extraction of chemicals (proteins/DNAs) from single living cells with much improved spatial resolution as compared to conventional delivery/extraction methods.

**Nanowires for Direct Solar to Fuel Conversion.** Nanowires, with their unique capability to bridge the nanoscopic and macroscopic worlds, have already been demonstrated as important materials for different energy conversion purposes, some of which have been discussed earlier in this article.

One emerging and exciting direction is their application for solar to fuel conversion. The generation of fuels by the direct conversion of solar energy in a fully integrated system is an attractive goal, but no such system has been demonstrated that shows the required efficiency, is sufficiently durable, or can be manufactured at reasonable cost. In this regard, one can learn a great deal from the nature's photosynthetic organisms that routinely carry out the conversion of carbon dioxide and water to carbohydrates. There are several key features in these photosynthetic systems: spatial and directional arrangement of the light-harvesting components, charge separation and transport, and the desired chemical conversion at catalytic sites in compartmentalized spaces.

To design an efficient artificial photosynthetic materials system, whether it is for water splitting or CO<sub>2</sub> reduction, we should try to use and optimize the same principle as much as possible. Semiconductor nanowires can be readily designed and synthesized to deterministically incorporate heterojunctions with improved light absorption, charge separation and directional transport. Meanwhile, it is also possible to selectively decorate different oxidation or reduction catalysts onto specific segments of the nanowires to mimic the compartmentalized reactions in Nature.

**Semiconductor nanowires can now be synthesized in large quantities using both gas phase and solution phase methods.**

More importantly, semiconductor nanowires can now be synthesized in large quantities using both gas phase and solution phase methods. The material compositions of interest include silicon, oxides (WO<sub>3</sub>, TiO<sub>2</sub>), nitrides (InGaN, Figure 6<sup>63</sup>), phosphides (GaP), chalcogenides (CdSe), and maybe others made of earth-abundant

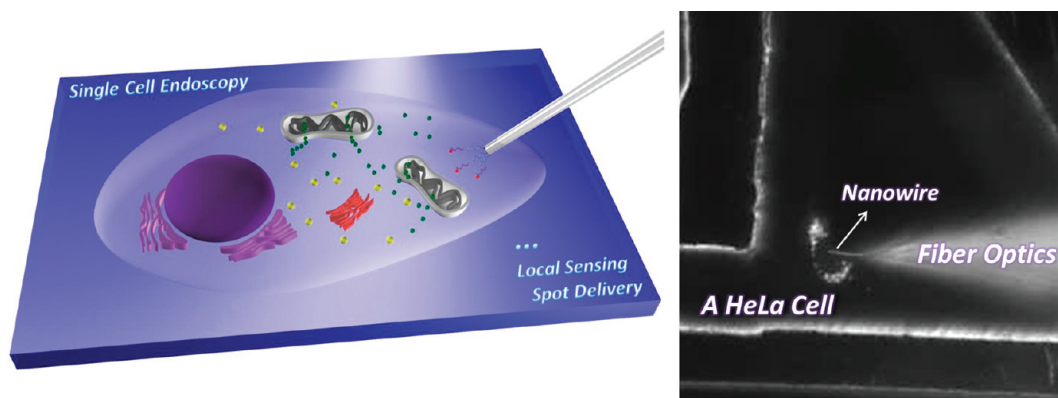


FIGURE 5. Nanowire-based biological probe for endoscopy, spot delivery, and sensing within a single living cell. The image on the right shows a single HeLa cell probed by an oxide nanowire attached onto an optical fiber tip.<sup>62</sup>

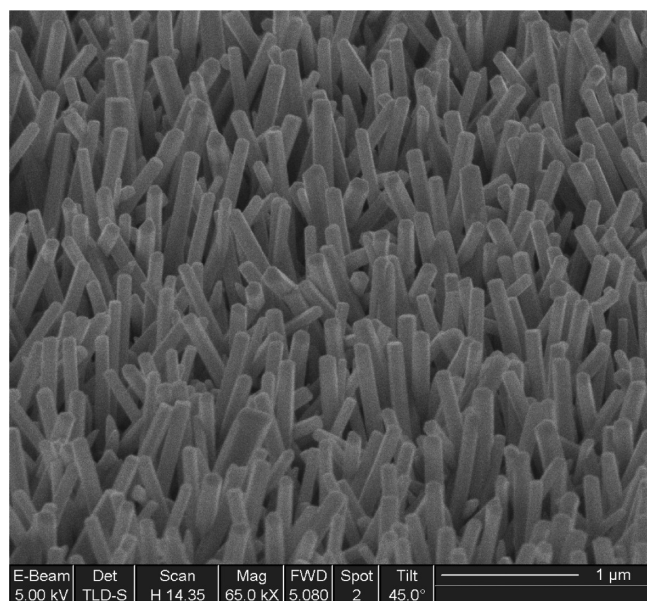


FIGURE 6. SEM image of an InGaN nanowire array, an potential light absorbing nanostructure for solar-to-fuel conversion purpose.<sup>63</sup>

elements. Because of their large aspect ratio and flexibility, nanowires can be readily processed (e.g., roll printing, casting, automated drawdown) into interconnecting meshes or membranes that are mechanically robust. In addition, the high surface-area nature of the nanowires often leads to a flexible mesh having a porous and open framework, which could greatly benefit the surface catalytic reaction as well as gas evolution. Having large surface-area and tunable electronic structures, these semiconductor nanowires could be used as effective photoelectrodes and are expected to play an important role in the overall solar-to-fuel system design.

As a result of the numerous fundamental breakthroughs made in the past decade, we now have a good understanding of the pros and cons of using semiconductor nanowires for various applications. We can also peer into the future with reasonable confidence.

We can be confident that we will continue to see many more fundamental new discoveries and science based on this unique class of nanoscale building blocks. Meantime, the future of nanowire technology will be largely dependent on how well we can balance the issues of cost, performance, and stability of the nanowire-based devices and systems.

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